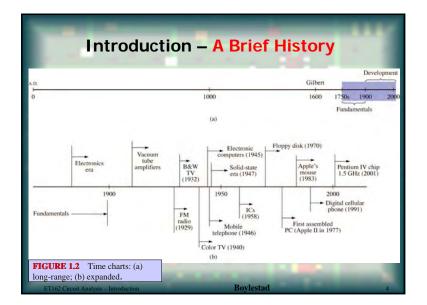
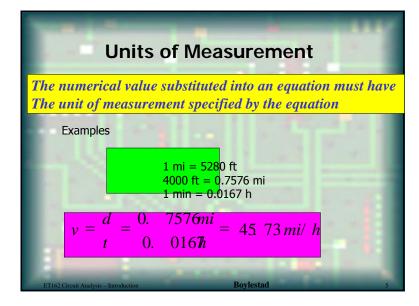
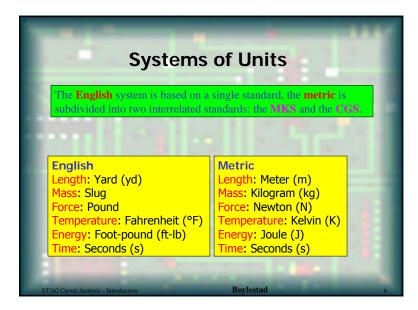


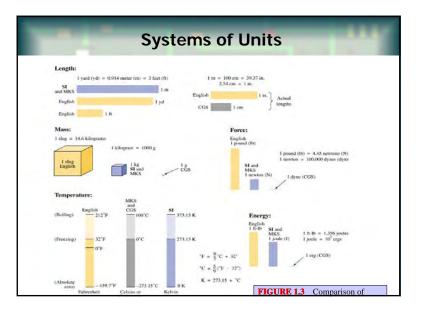


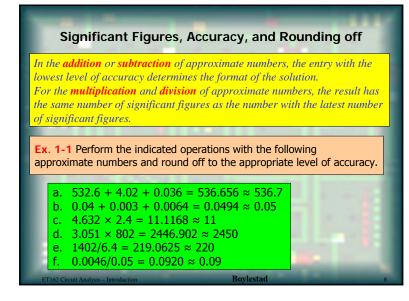
- Fiber Optics & Opto-Electronics
- Integrated Circuit (IC)

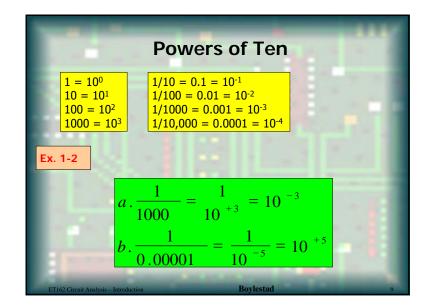


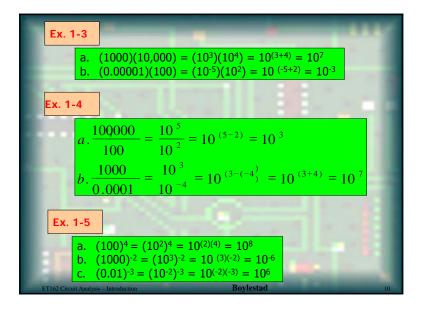


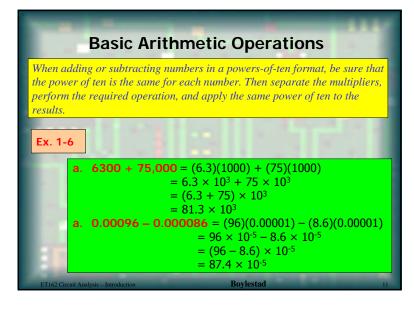


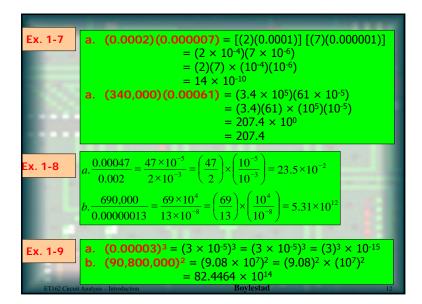




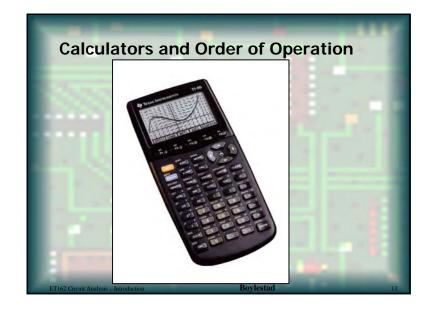


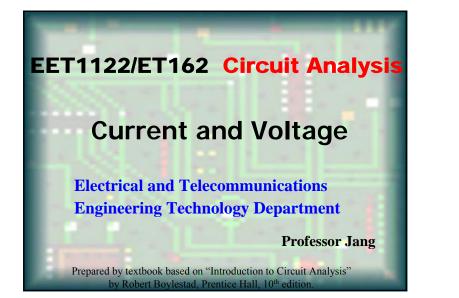


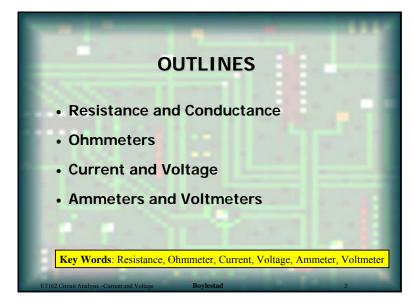


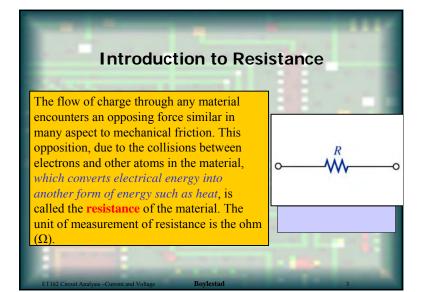


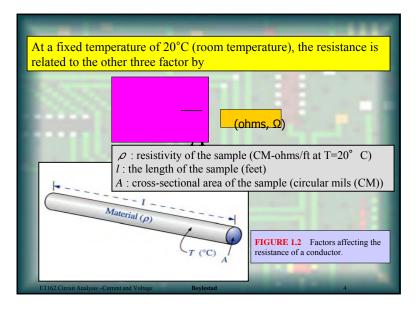
HW 1-26 Perform the following conversions: a. 1.5 min to seconds b. 0.04 h to seconds c. 0.05 s to microseconds d. 0.16 m to millimeters e. 0.00000012 s to nanoseconds f. 3,620,000 s to days	4, 26, 41, 42, 43
a. $1.5 \text{ m/m} \left[\frac{60 \text{ s}}{1 \text{ m/m}} \right] = 90 \text{ s}$ b. $0.04 \text{ //} \left[\frac{60 \text{ m/m}}{1 \text{ //}} \right] \left[\frac{60 \text{ s}}{1 \text{ m/m}} \right] = 144 \text{ s}$	
c. $0.05 \varkappa \left[\frac{1 \mu s}{10^{-6} \wp} \right] = 0.05 \times 10^{6} \mu s = 50 \times 10^{3} \mu s$ d. $0.16 \varkappa \left[\frac{1 \text{ mm}}{10^{-3} \text{ m/s}} \right] = 0.16 \times 10^{3} \text{ mm} = 160 \text{ mm}$	
e. $1.2 \times 10^{-7} \text{s} \left[\frac{1 \text{ns}}{10^{-9} \text{s}'} \right] = 1.2 \times 10^{2}$ f. $3.62 \times 10^{6} \text{s} \left[\frac{1 \text{pmn}}{60 \text{s}'} \right] \left[\frac{1 \text{k}'}{60 \text{pm}} \right] \left[\frac{1}{60 \text{pm}} \right]$	
ET162 Circuit Analysis – Introduction Boylestad	14

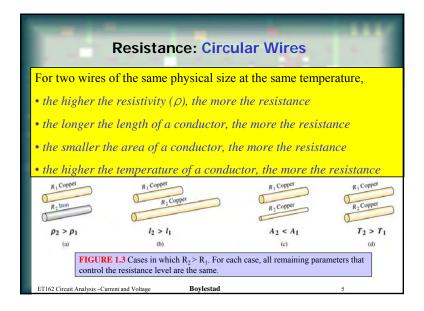


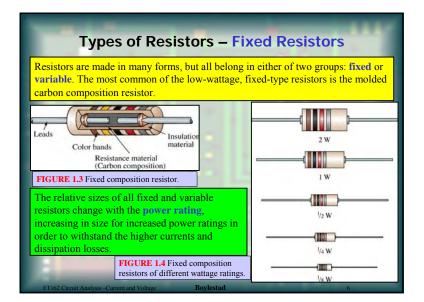


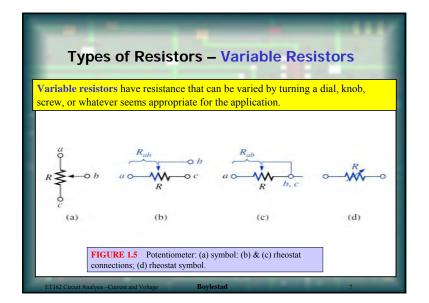






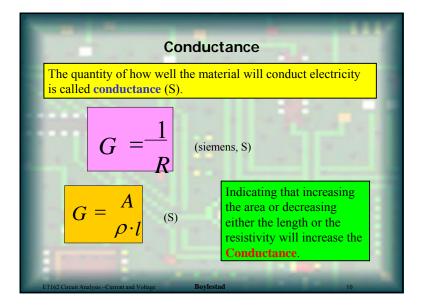




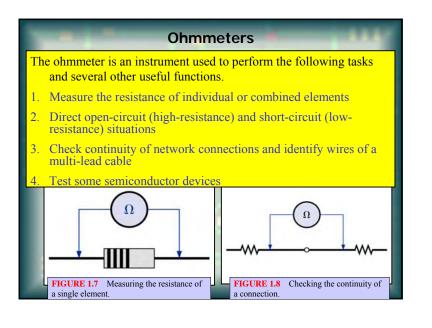


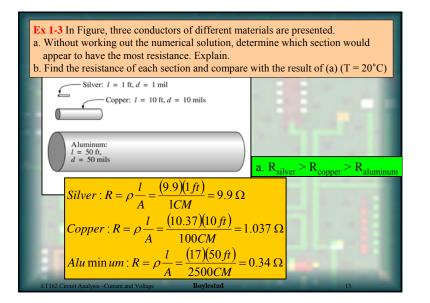
Color Coding a				_
A whole variety of resistor ohms printed on the casing printed on them, so a syste	. However, son	ne are too s	mall to have n	
12345	FIGURE 1.6 Color coding of fixed molded co		ixed molded con	position resistor.
	Band 1-2	Band 3	Band 4	Band 5
	0 Black	100	5% Gold	1% Brown
	1 Brown	101	10% Silver	0.1% Red
The first and second bands	2 Red	10 ²	20% No band	0.01% Orange
represent the first and second	3 Orange	10 ³		0.01% Yellow
digits, respectively. The third	4 Yellow	104		
band determines the power-of-	5 Green	105		
ten multiplier for the first two	6 Blue	106		
digits. The fourth band is the manufacture's tolerance. The	7 Violet	107		
fifth band is a reliability factor	8 Gray	108		
which gives the percentage of	9 White	109		
failure per 1000 hours of use.	1	able 1 Resiste	or color	8

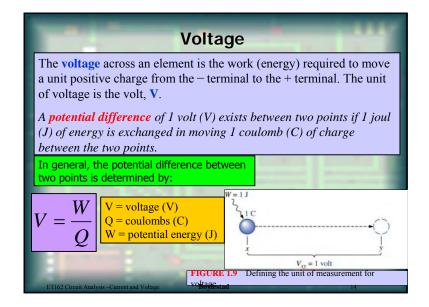
xist to	satisfy the man		istor having the erance:	ionowing core	i bands must
_					_
b.	1 st Band	2 nd Band	3rd Band	4 th Band	5 th Band
	Orange	White	Gold	Silver	No color
	3	9	10-1 = 0.1	±10%	
a. 8	$\frac{1}{2\Omega \pm 5\%}$ (1%)	reliability)			
Sin	$c_{2} = 5\% \text{ of } 82 - 4$	10 the resisto	r should be with	ain the range of	F 820 +
	0Ω , or between	· · · · · · · · · · · · · · · · · · ·		init the range of	0232 -
	,				
	$.9\Omega \pm 10\% = 3$	$0.9\Omega \pm 0.39\Omega$			
b. 3					



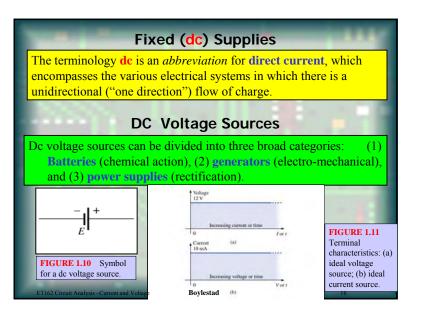
Boylestad

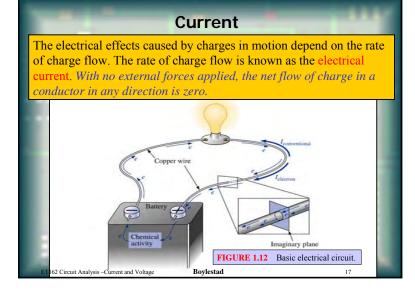


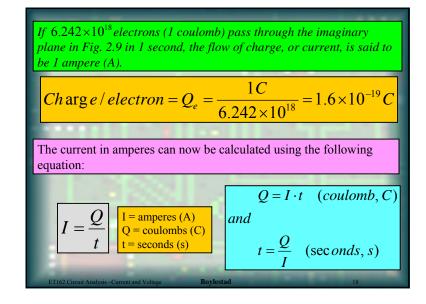


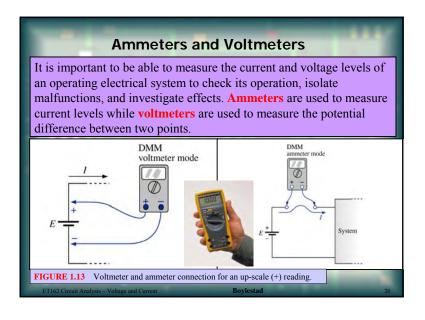


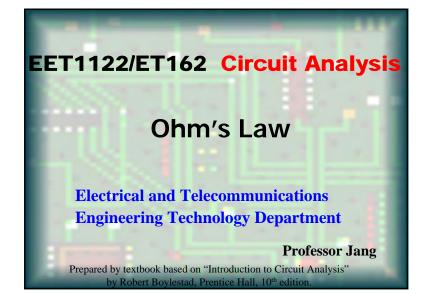
Ex. 1-4 Find the potential difference between two points in an electrical system
if 60 J of energy are expended by a charge of 20 C between these two points.
$$V = \frac{W}{Q} = \frac{60 J}{20 C} = 3 V$$
Ex. 1-5 Determine the energy expended moving a charge of 50 µC through a
potential difference of 6 V.
$$W = Q \cdot V = (50 \times 10^{-6})(6 V)$$
$$= 300 \times 10^{-6} J = 300 \mu J$$

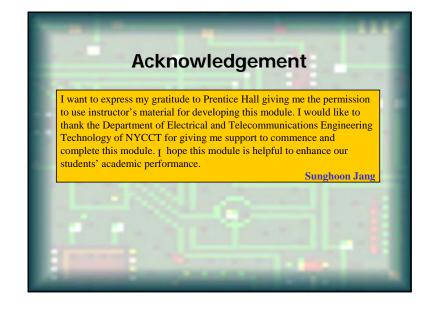


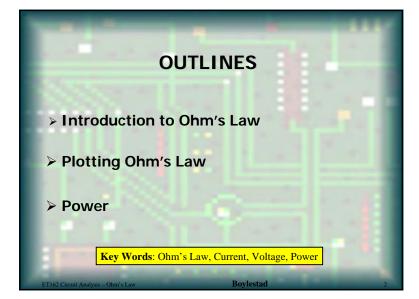


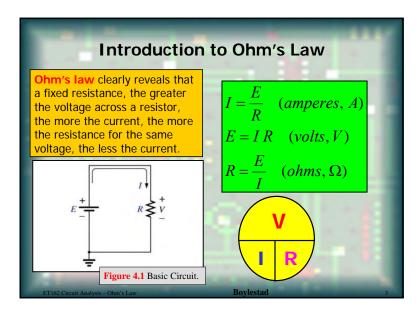


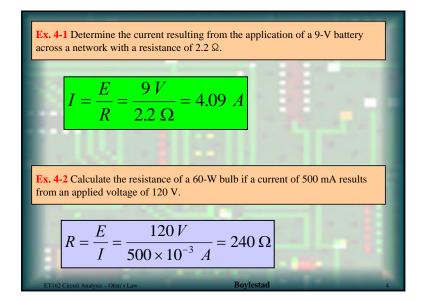


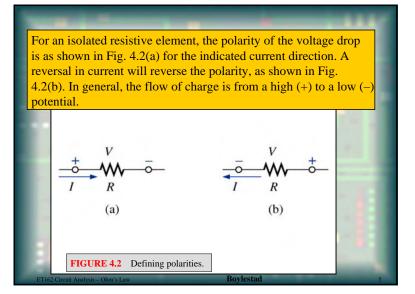


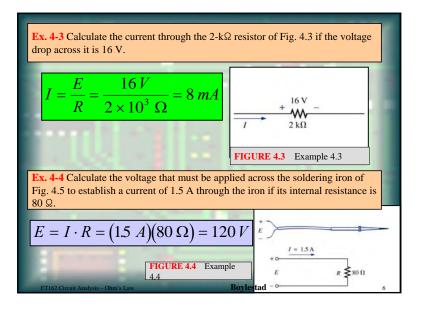


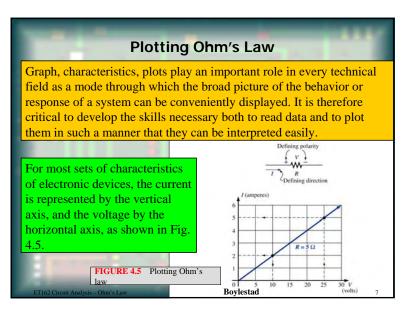




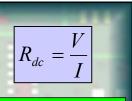




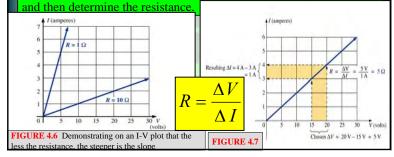


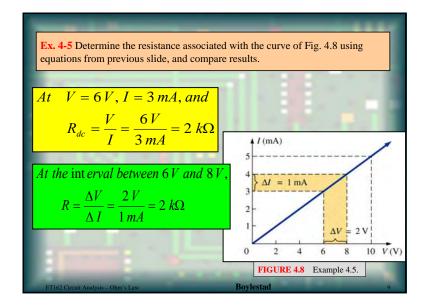


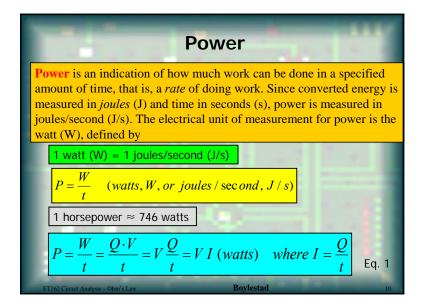
If the resistance of a plot is unknown, it can be determined at any point on the plot since a straight line indicates a fixed resistance. At any point on the plot, find the resulting current and voltage, and simply substitute into following equation:

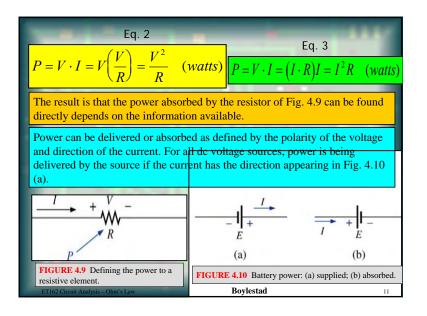


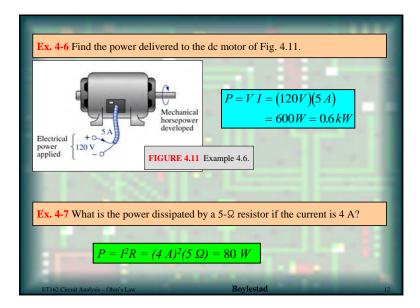
The equation states that by choosing a particular ΔV , one can obtain the corresponding ΔI from the graph, as shown in Fig. 4.6 and 4.7,

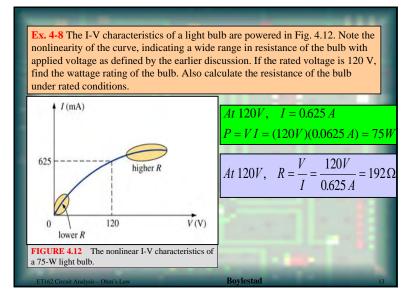












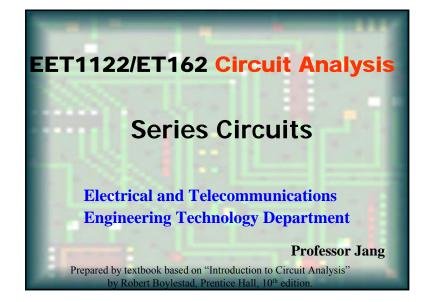
Sometimes the power is given and the current or voltage must be determined.

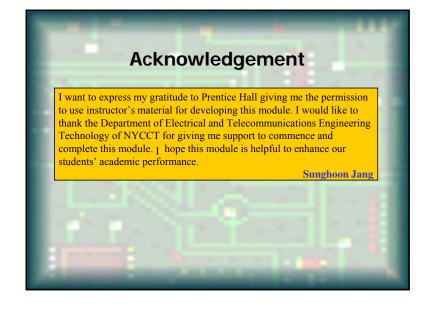
$$P = I^2 R \Rightarrow I^2 = \frac{P}{R} \quad or \quad I = \sqrt{\frac{P}{R}} \quad (ampere)$$

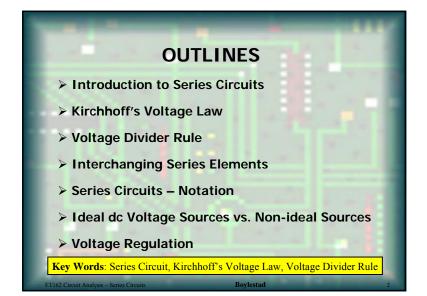
$$P = \frac{V^2}{R} \Rightarrow V^2 = PR \quad or \quad V = \sqrt{PR} \quad (volts)$$
Ex. 4-9 Determine the current through a 5-k Ω resistor when the power dissipated by the element is 20 mW.

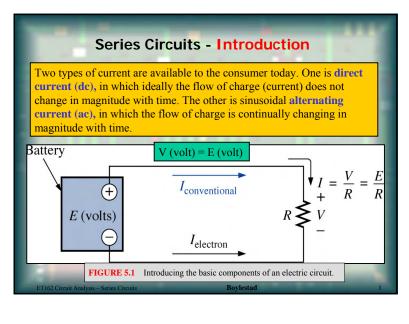
$$I = \sqrt{\frac{P}{R}} = \sqrt{\frac{20 \times 10^{-3} W}{5 \times 10^{3} \Omega}} = \sqrt{4 \times 10^{-6}} = 2 \times 10^{-3} A = 2 mA$$

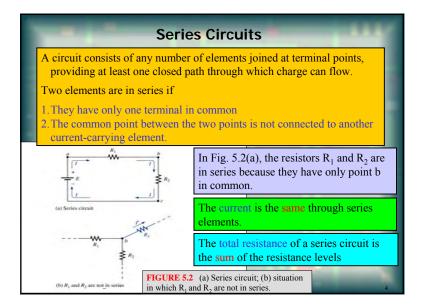
HW 4-52 A stereo system draws 2.4 A at 120 V. The audio output power is 50 W. a. How much power is lost in the form of heat in the system? b. What is the efficiency of the system? $a. \quad P_i = EI = (120V)(2.4 A) = 288 W$ $P_i = P_o + P_{lost}, \quad P_{lost} = P_i - P_0 = 288 W - 50 W = 238 W$ $b. \quad \eta\% = \frac{P_0}{P_i} = 100\% = \frac{50W}{288W} \times 100\% = 17.36\%$ Homework 4: 2, 4, 6, 8, 20, 24, 25, 26, 49, 52 Homework 4: 2, 4, 6, 8, 20, 24, 25, 26, 49, 52

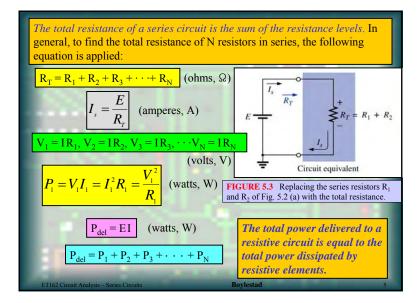


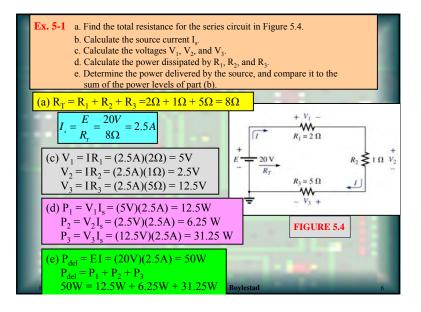


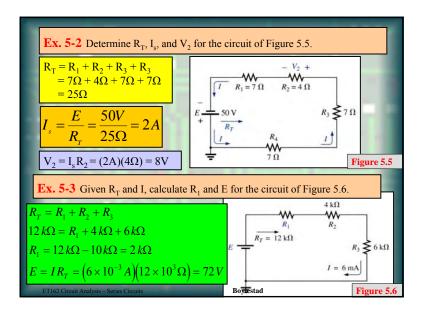


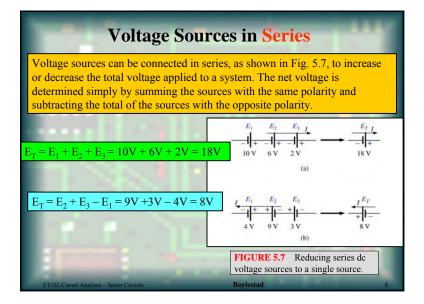


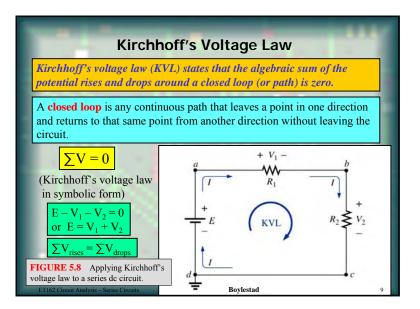


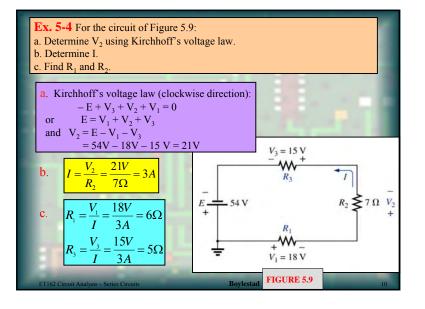


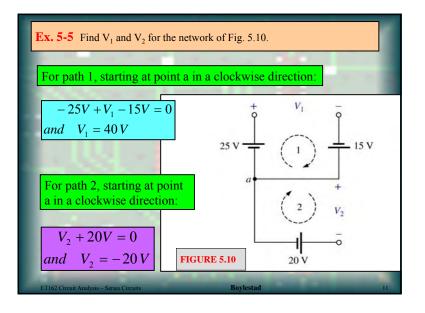


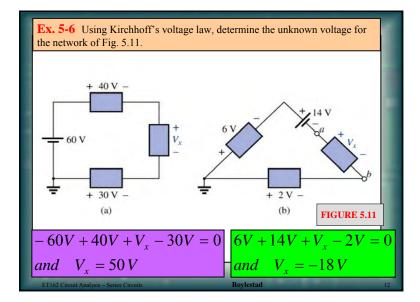


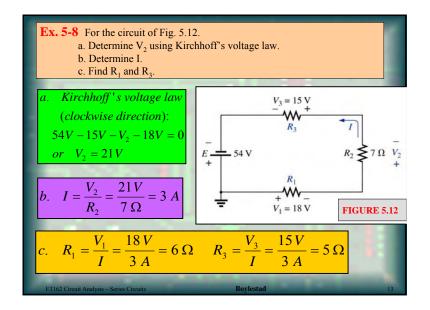


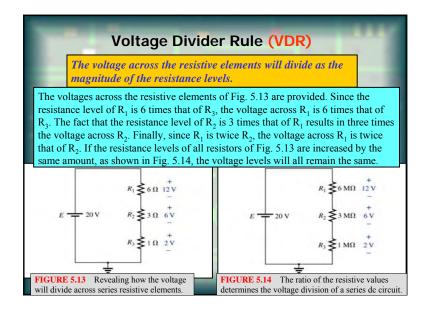


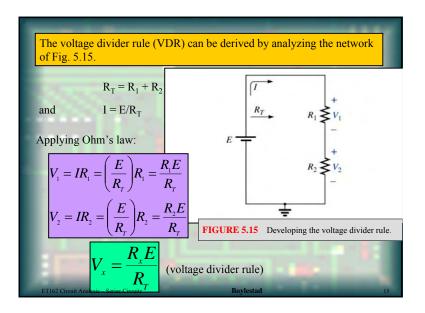


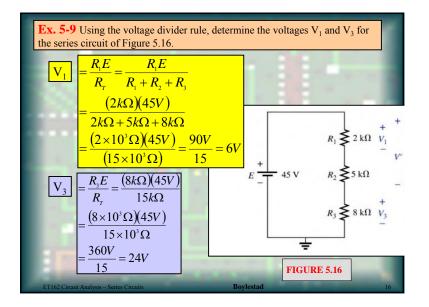


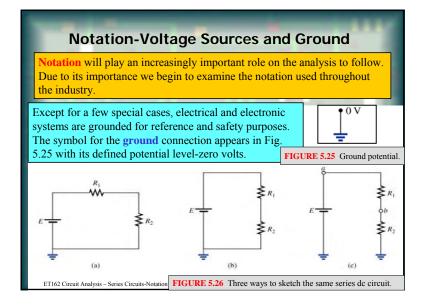


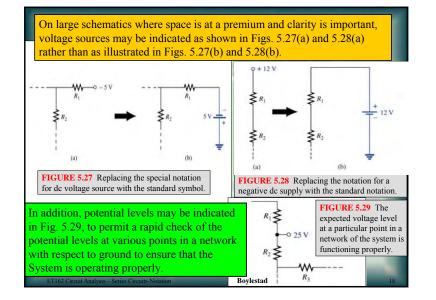


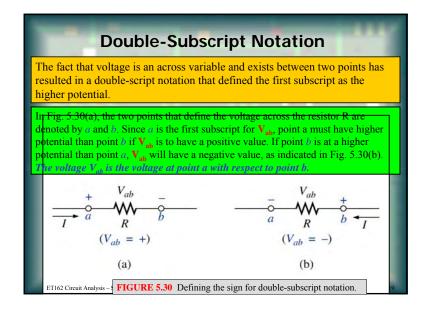


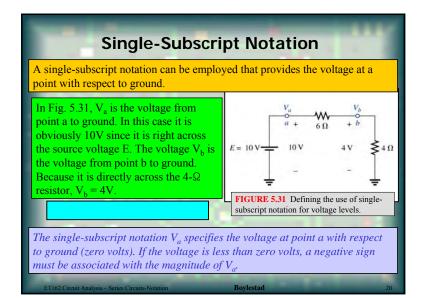


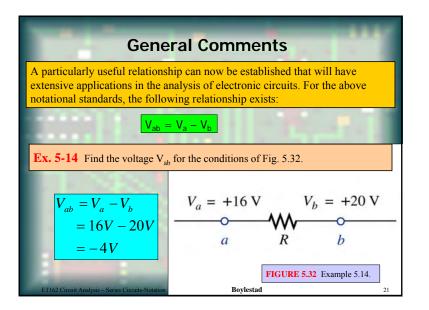


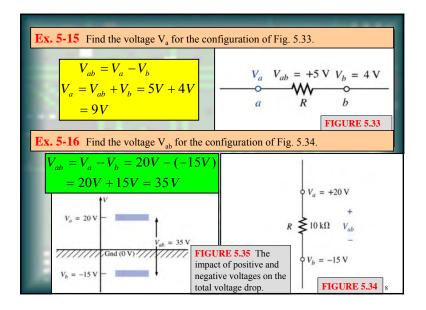


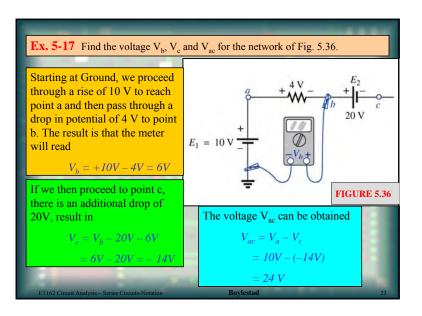


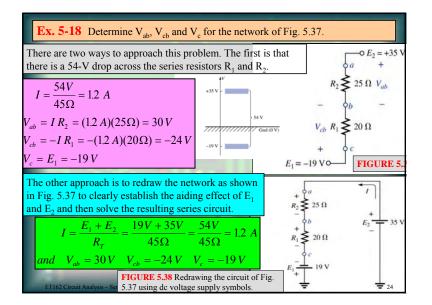


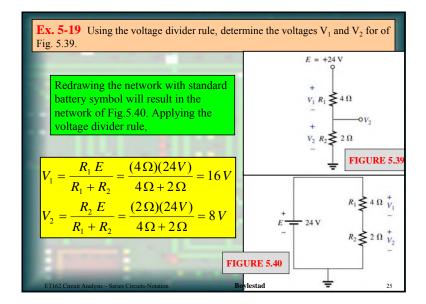


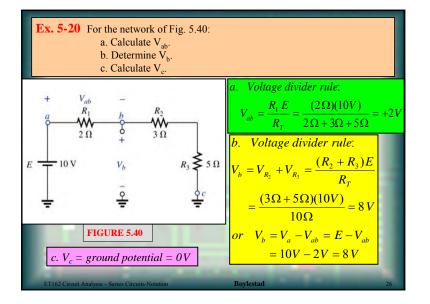


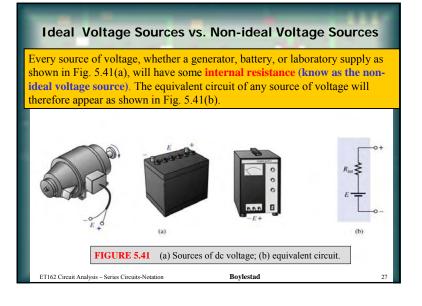




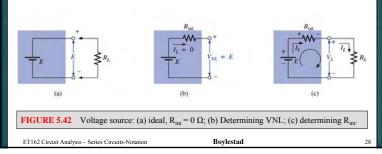


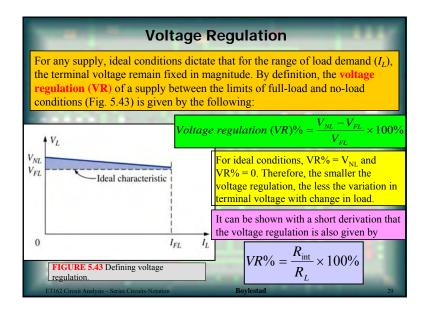


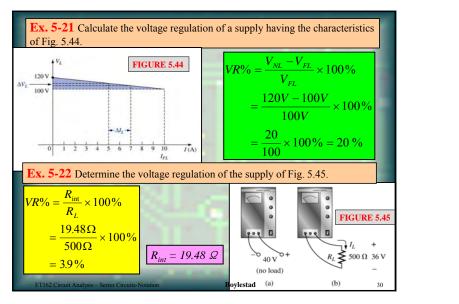


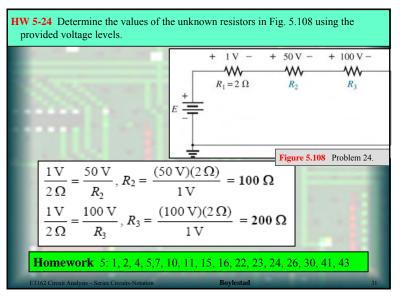


In all the circuit analyses to this point, **the ideal voltage source** (no internal resistance) was used shown in Fig. 5.42(a). The ideal voltage source has no internal resistance and an output voltage of *E* volts with no load or full load. In the practical case [Fig. 5.42(b)], where we consider the effects of the internal resistance, the output voltage will be *E* volts only when no-load ($I_L = 0$) conditions exist. When a load is connected [Fig. 5.42(c)], the output voltage of the voltage drop across the internal resistance.

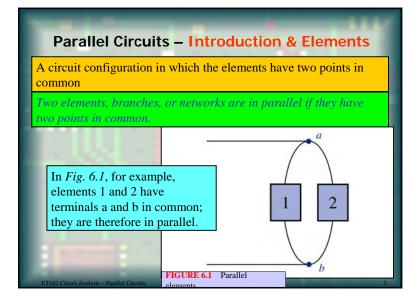


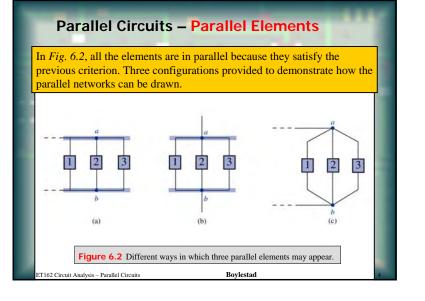


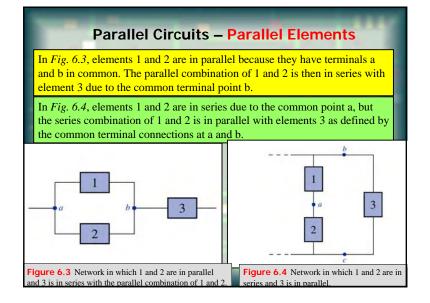


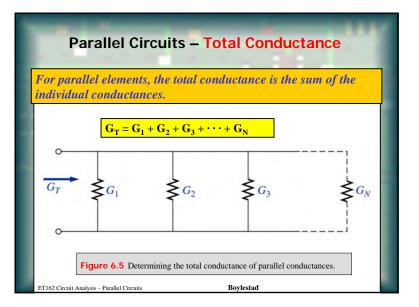


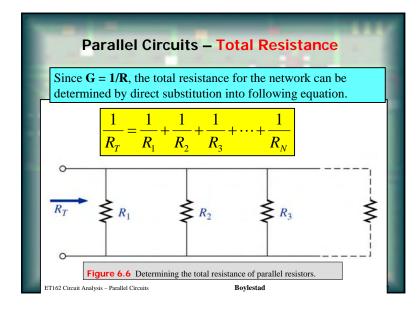


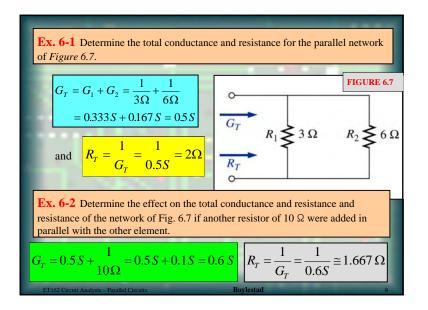


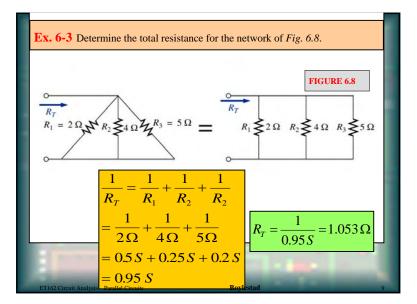


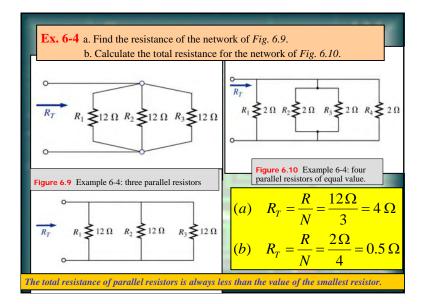


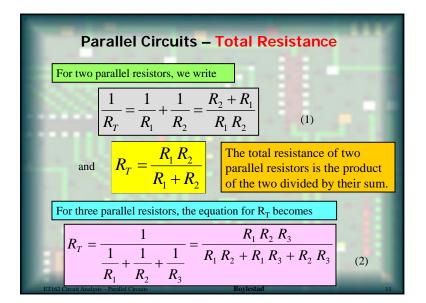


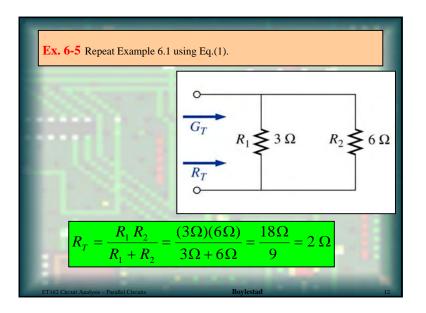


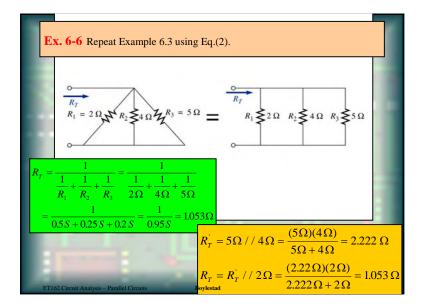


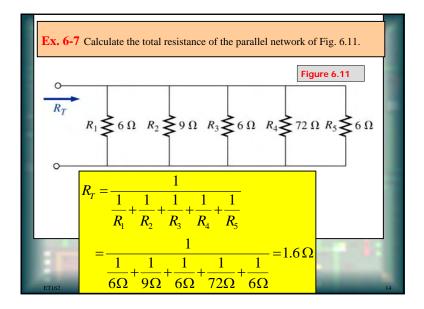


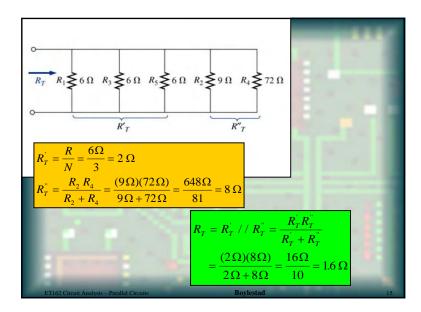


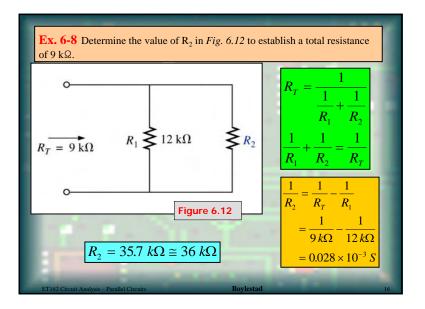


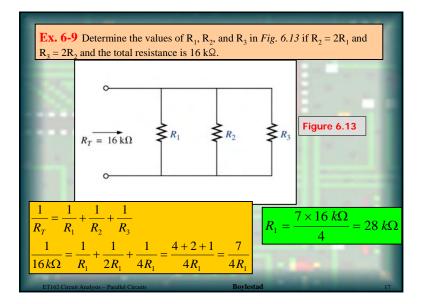


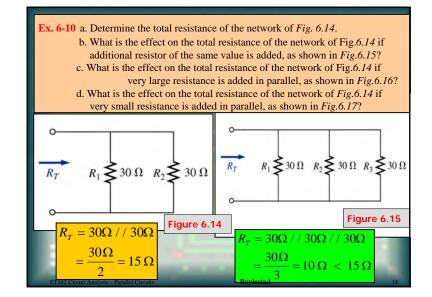


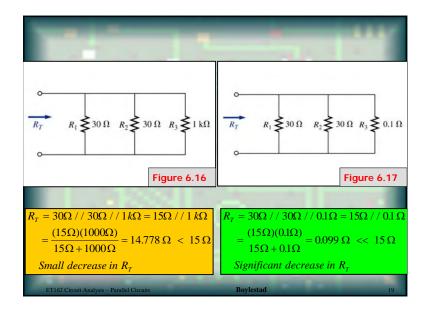


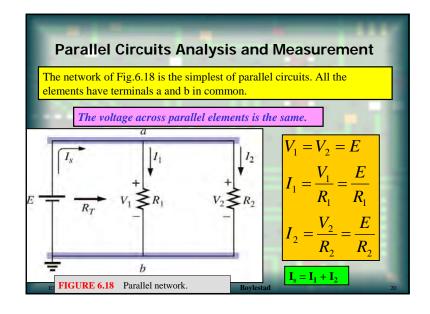


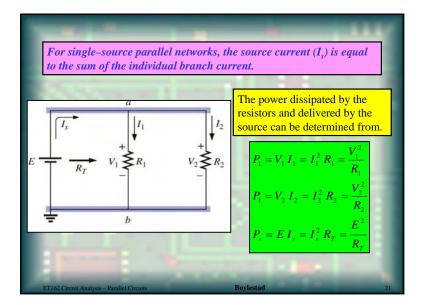


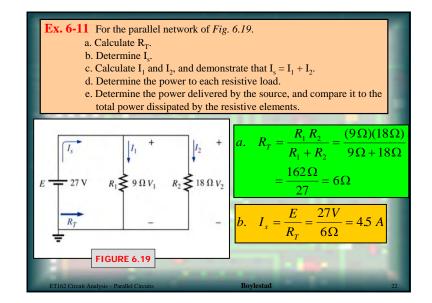


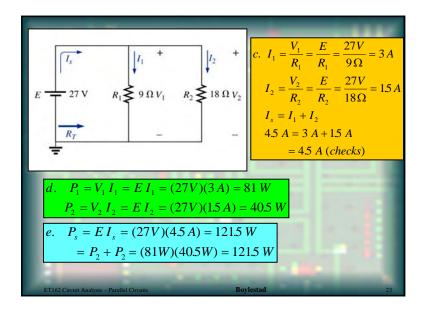


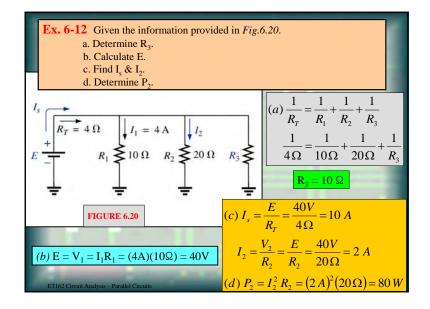


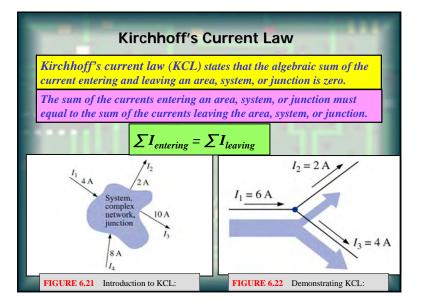


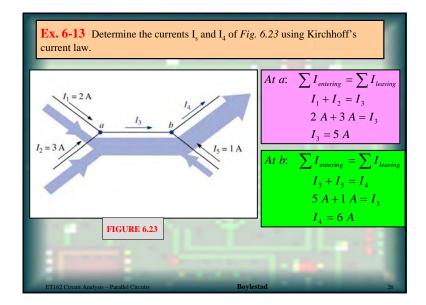


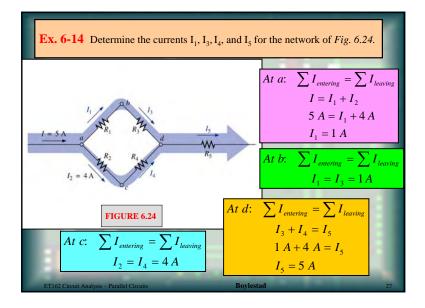


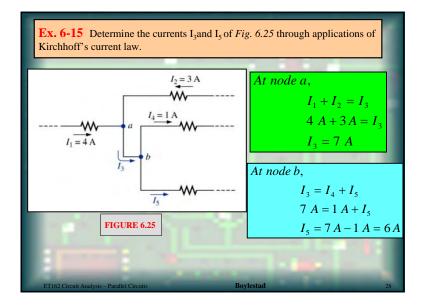


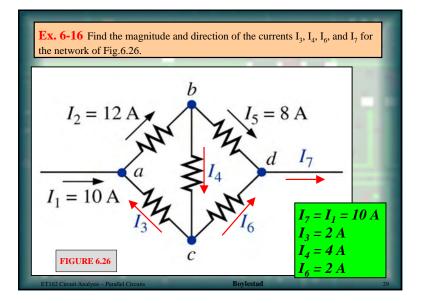


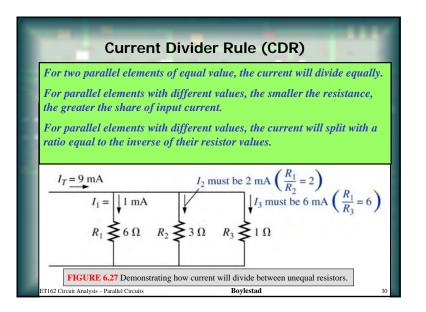


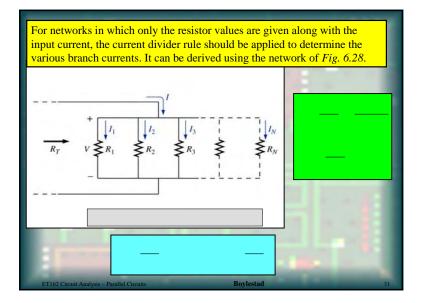


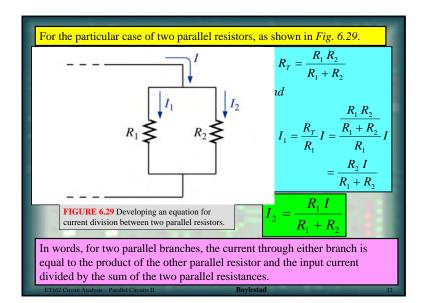


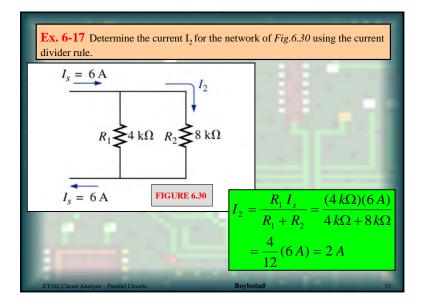


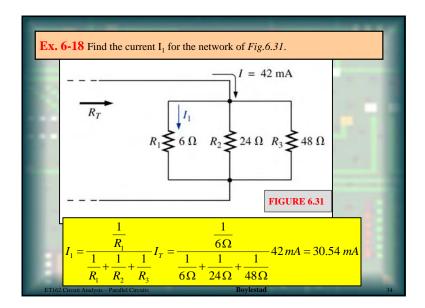


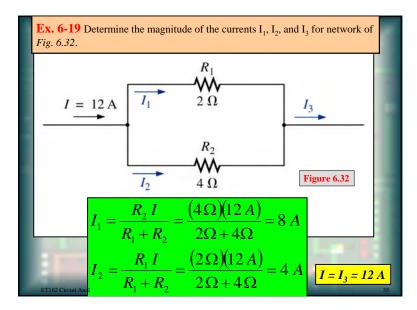


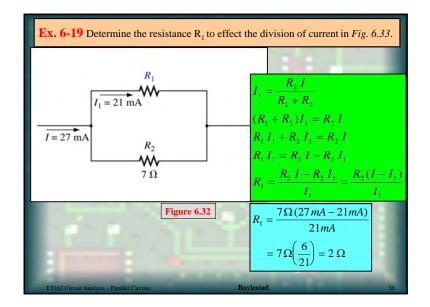


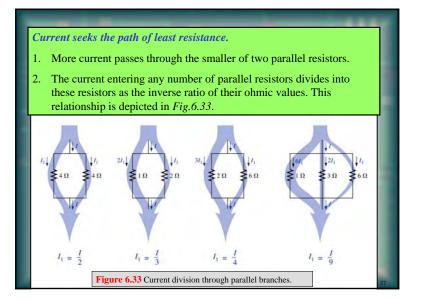


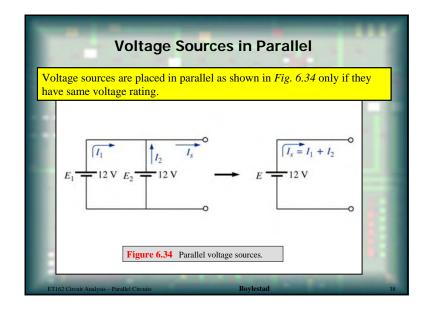


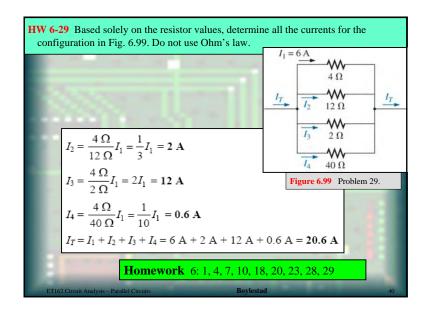






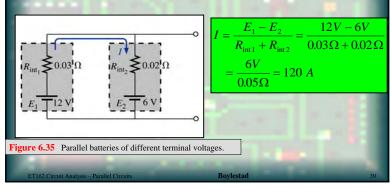


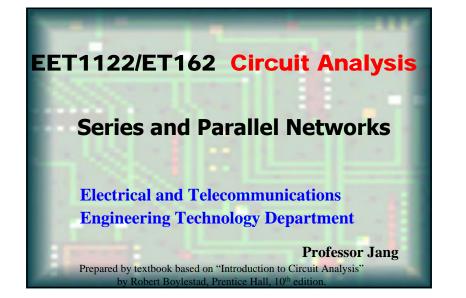


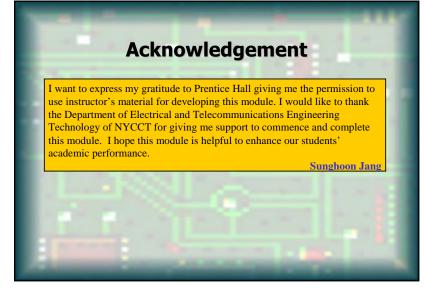


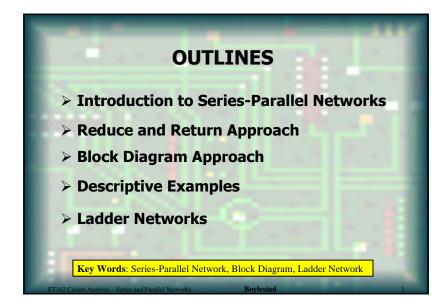
If two batteries of different terminal voltages were placed in parallel both would be left ineffective or damaged because the terminal voltage of the larger battery would try to drop rapidly to that of the lower supply.

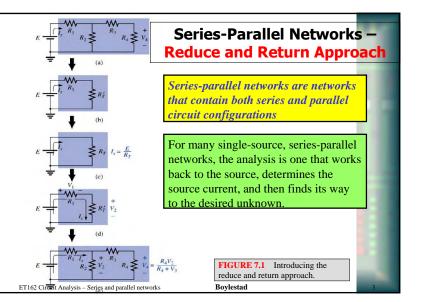
Consider two lead-acid car batteries of different terminal voltage placed in parallel, as shown in *Fig. 6. 35*.

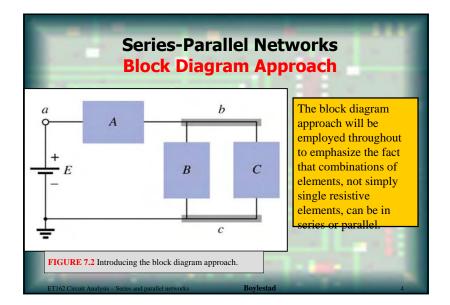


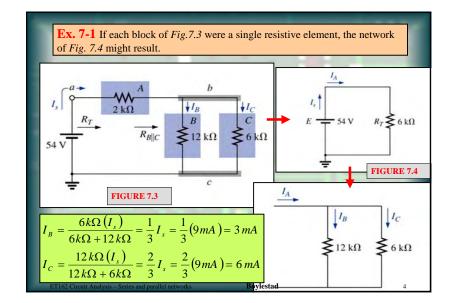


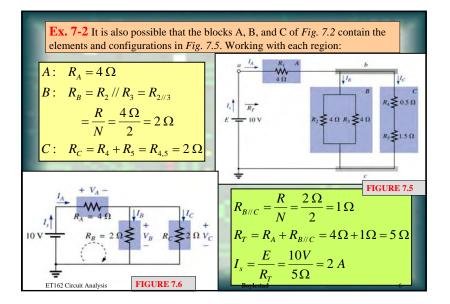


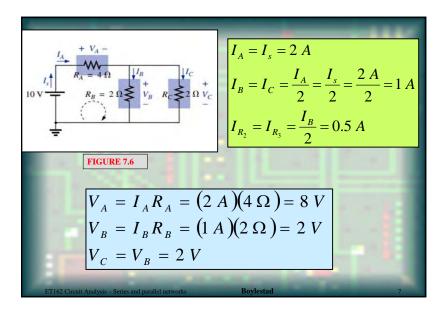


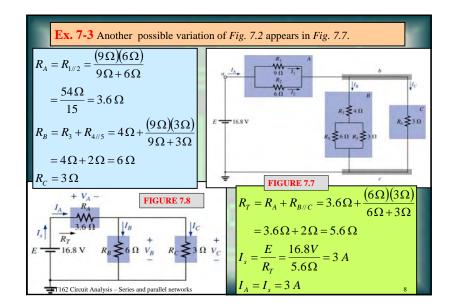


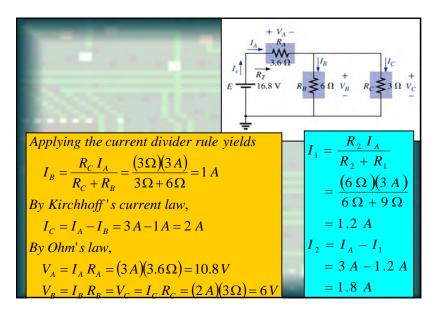


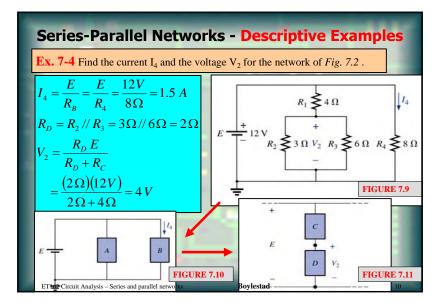


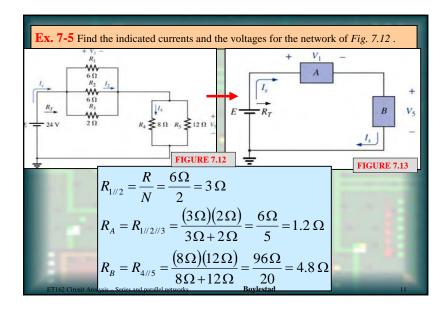


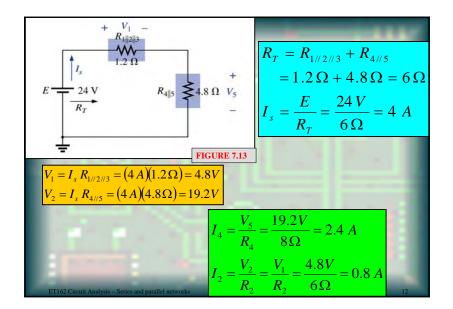


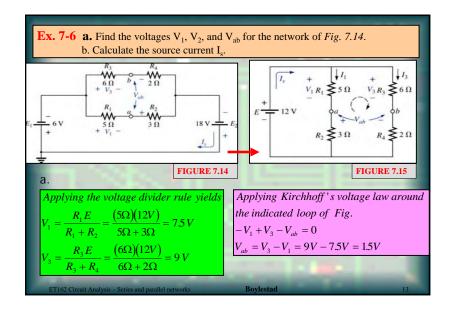


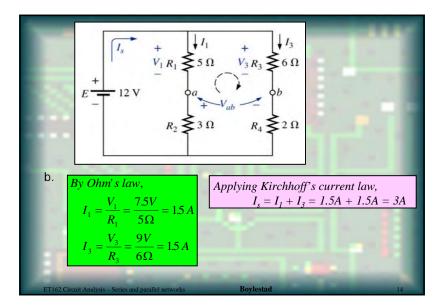


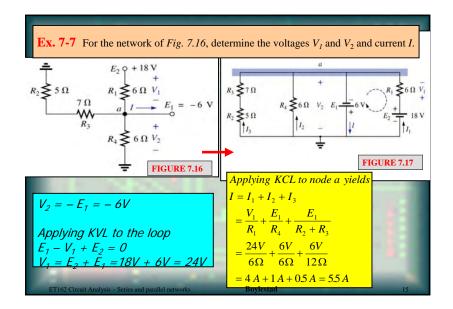


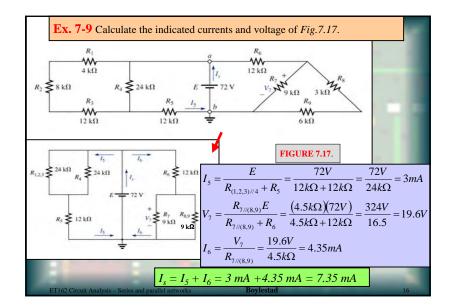


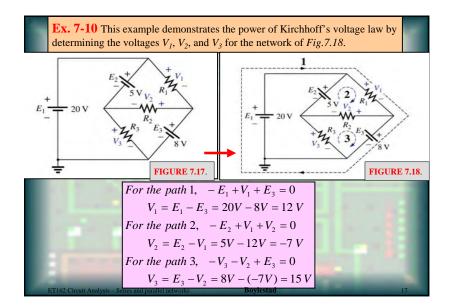


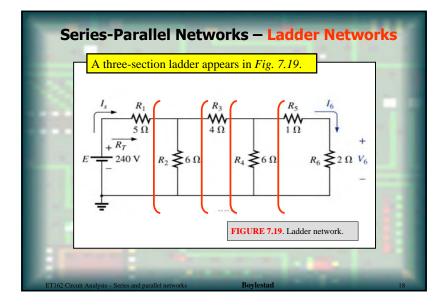


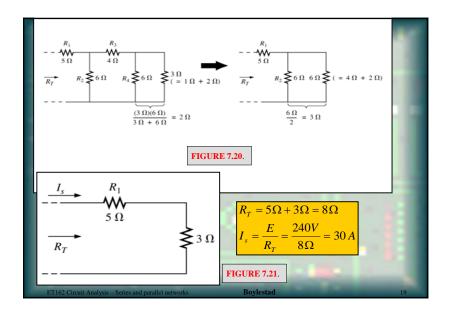


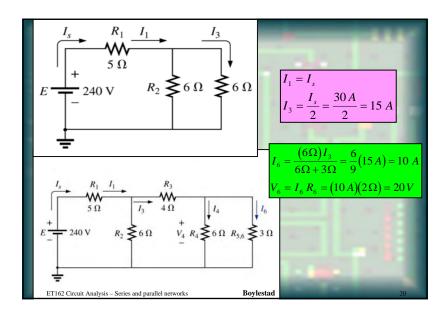


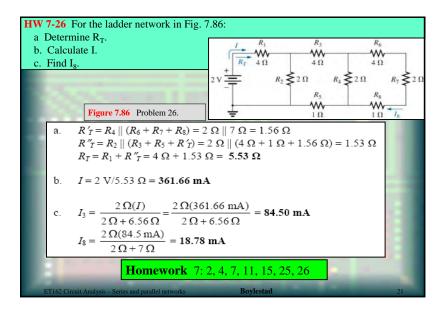


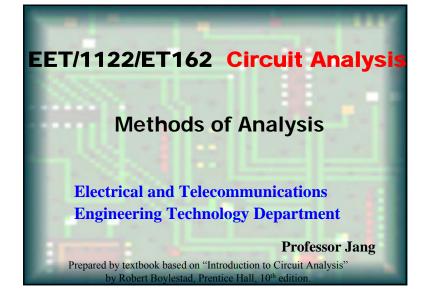




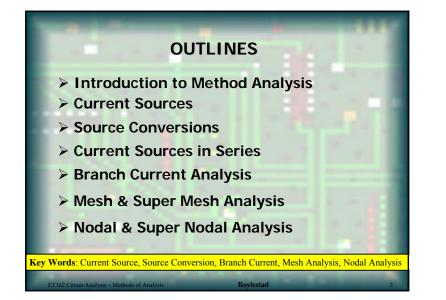








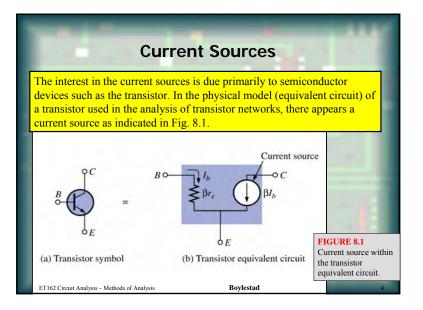


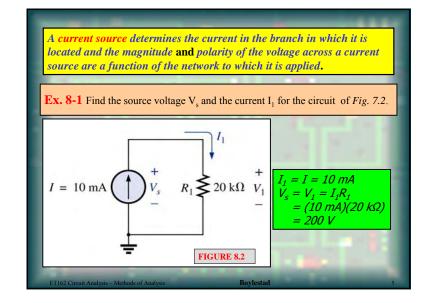


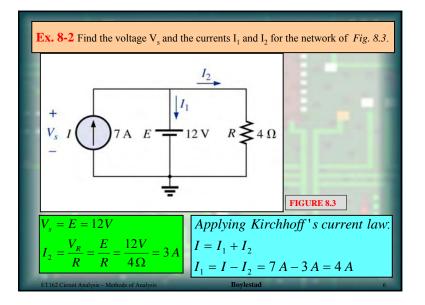
Introduction to Methods of Analysis

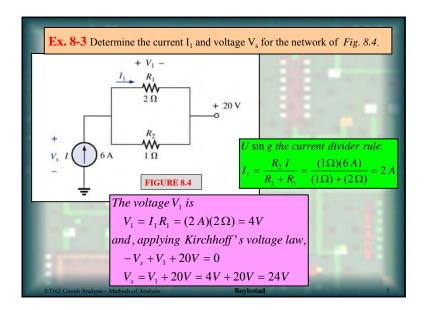
The circuits described in the previous chapters had only one source or two or more sources in series or parallel present. The step-by-step procedure outlined in those chapters cannot be applied if the sources are not in series or parallel.

Methods of analysis have been developed that allow us to approach, in a systematic manner, a network with any number of sources in any arrangement. **Branch-current analysis**, **mesh analysis**, and **nodal analysis** will be discussed in detail in this chapter.



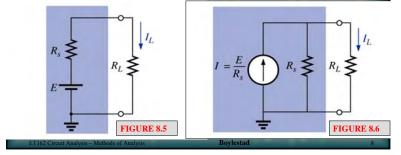






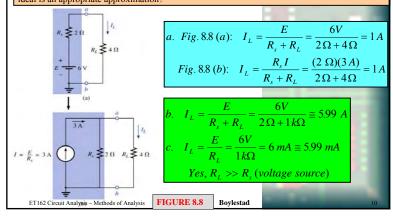
Source Conversions

All sources-whether they are voltage or current-have some internal resistance in the relative positions shown in *Fig. 8.5* and *8.6*. For the voltage source, if $R_s = 0 \Omega$ or is so small compared to any series resistor that it can be ignored, then we have an "ideal" voltage source. For the current source, if $R_s = \infty \Omega$ or is large enough compared to other parallel elements that it can be ignored, then we have an "ideal" current source.

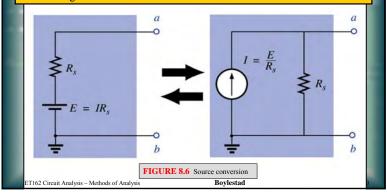


Ex. 8-4 a. Convert the voltage source of Fig. 8.8 (a) to a current source, and calculate the current through the 4- Ω load for each source.

b. Replace the 4- Ω load with a 1-k Ω load, and calculate the current I_L for the voltage source. c. Replace the calculation of part (b) assuming that the voltage source is ideal (R_s = 0 Ω) because R_L is so much larger than R_s. Is this one of those situations where assuming that the source is ideal is an appropriate approximation?

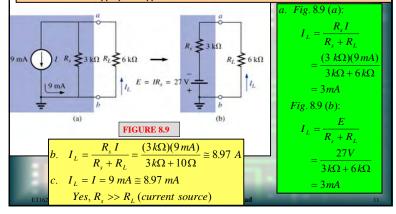


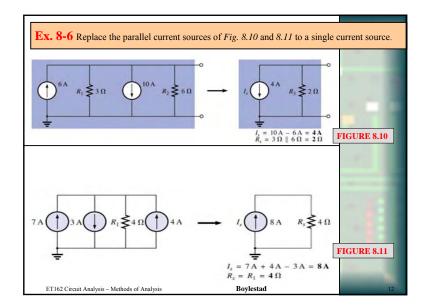
The equivalent sources, as far as terminals a and b are concerned, are repeated in *Fig. 8.7* with the equations for converting in either direction. Note, as just indicated, that the resistor R_s is the same in each source; only its position changes. The current of the current source or the voltage of the voltage source is determined using Ohm's law and the parameters of the other configuration.

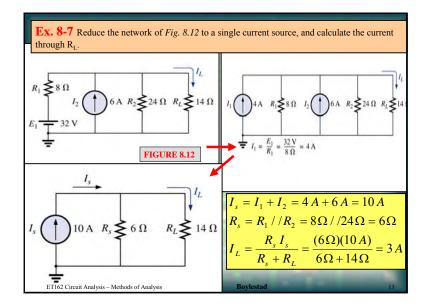


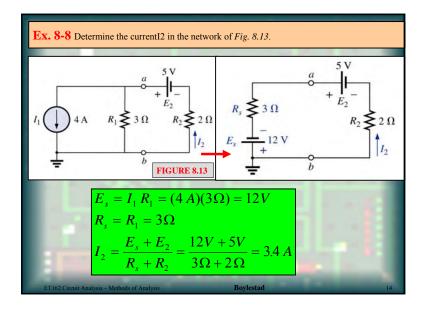
Ex. 8-5 a. Convert the current source of *Fig.* 8.9(a) to a voltage source, and find the load current for each source.

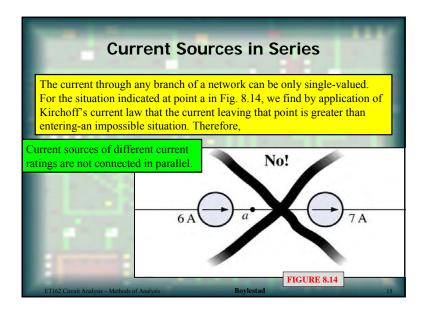
b. Replace the 6-k Ω load with a 10-k Ω load, and calculate the current I_L for the current source. **c**. Replace the calculation of part (b) assuming that the vourrent source is ideal ($R_s = \infty \Omega$) because R_L is so much smaller than R_s . Is this one of those situations where assuming that the source is ideal is an appropriate approximation?

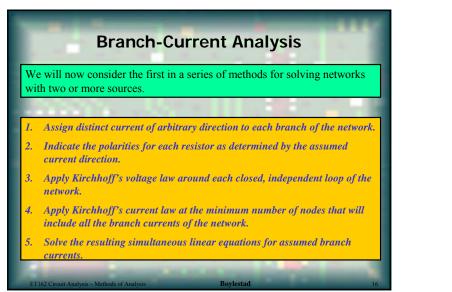


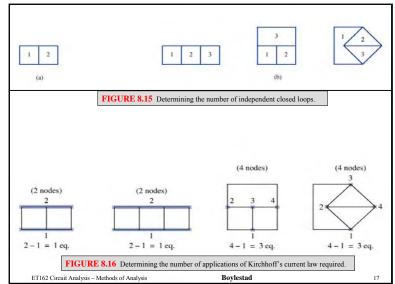


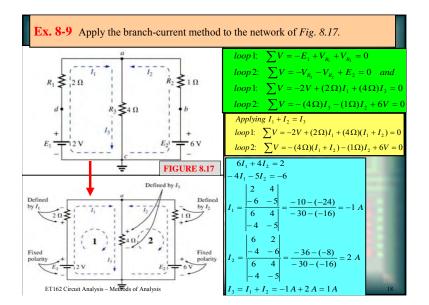


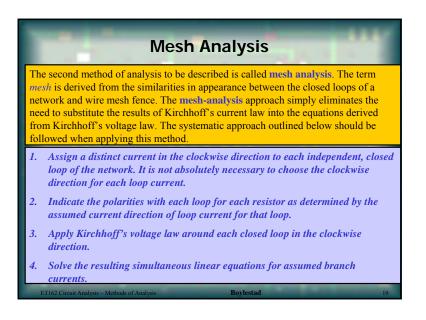


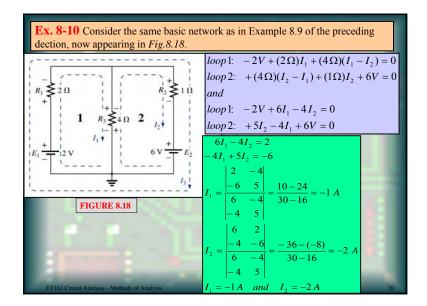


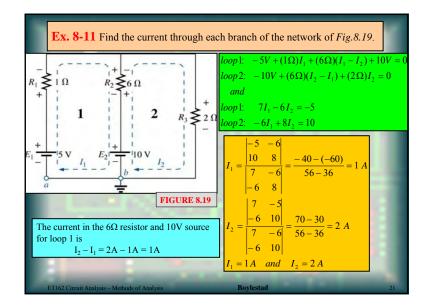


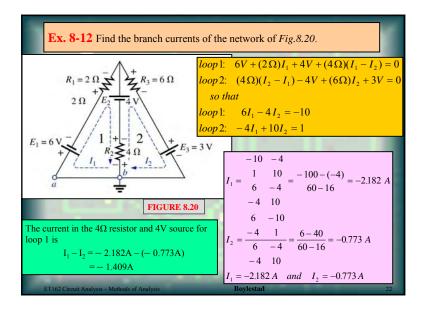


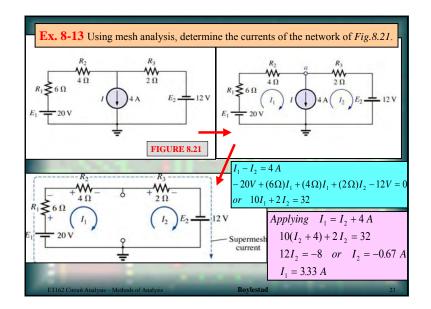












Nodal Analysis

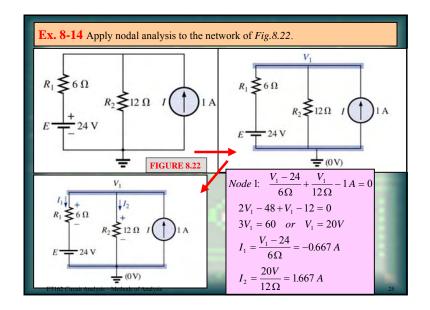
We will employ Kirchhoff's current law to develop a method referred to as **nodal analysis**. A node is defined as a junction of two or more branches. Since a point of zero potential or ground is used as a reference, the remaining nodes of the network will all have a fixed a fixed potential relative to this reference. For a network of N nodes, therefore, there will exist (N - 1) nodes.

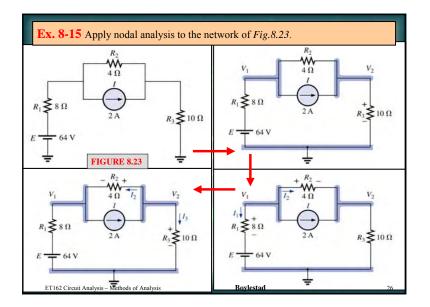
- 1. Determine the number of nodes within the network.
- 2. Pick a reference node, and label each remaining node with a subscripted value of voltage: V_p , V_2 , and so on.
- 3. Apply Kirchhoff's current law at each node except the reference. Assume that all unknown currents leave the node for each application of Kirchhoff's current law.

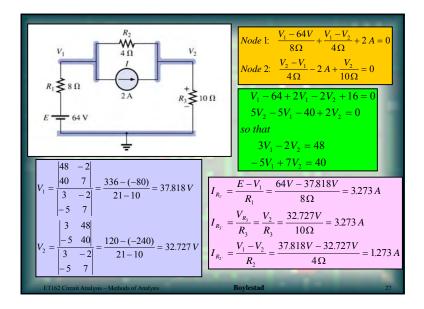
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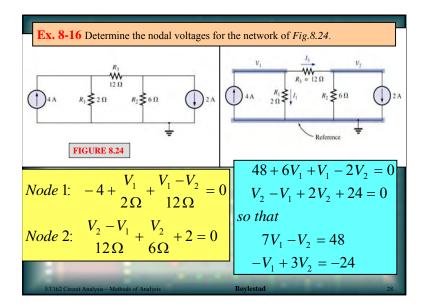
4. Solve the resulting equations for the nodal voltages.

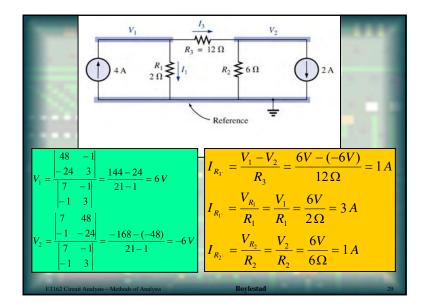
T162 Circuit Analysis - Methods of Analysi

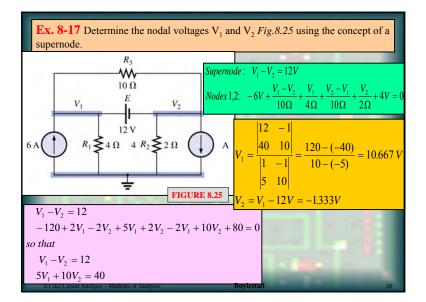


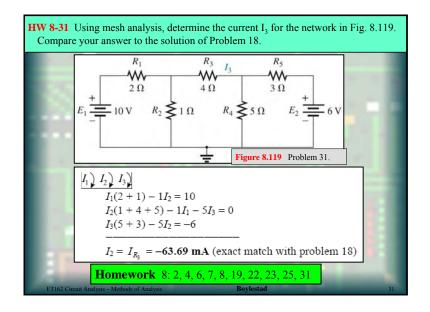


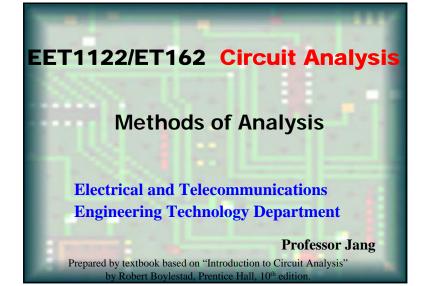




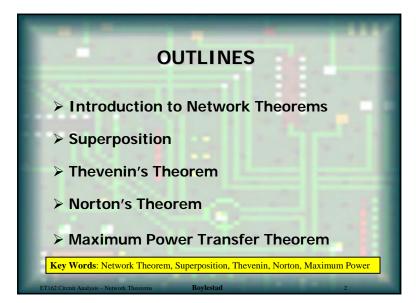








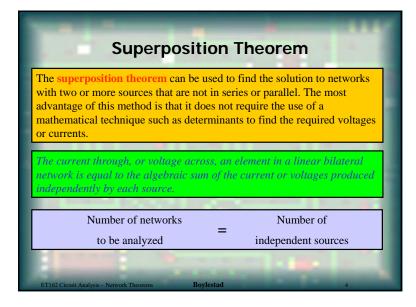


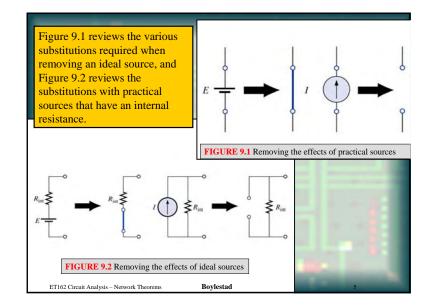


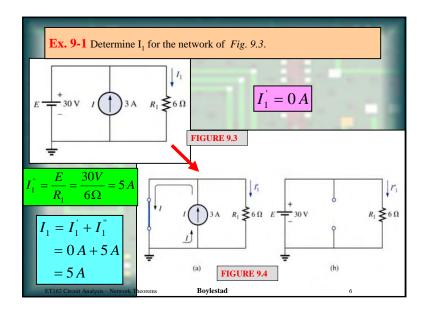
Introduction to Network Theorems

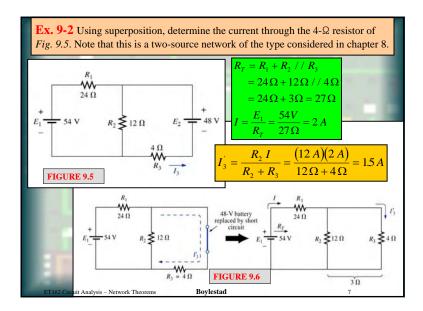
This chapter will introduce the important fundamental theorems of network analysis. Included are the superposition, **Thevenin's, Norton's, and maximum power transfer theorems.** We will consider a number of areas of application for each. A through understanding of each theorems will be applied repeatedly in the material to follow.

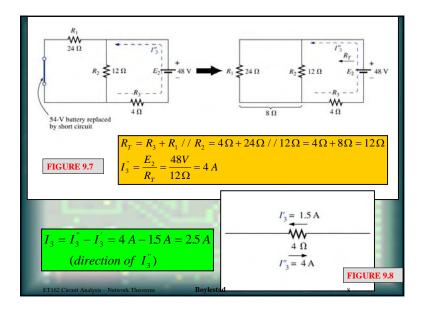


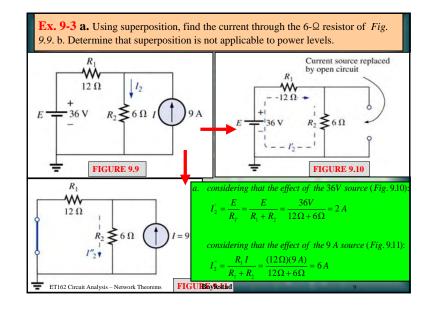


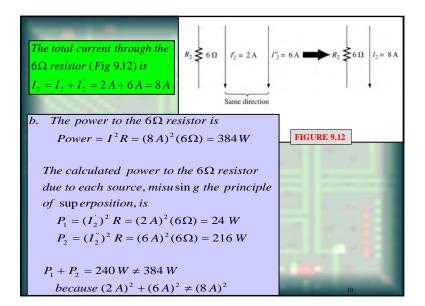


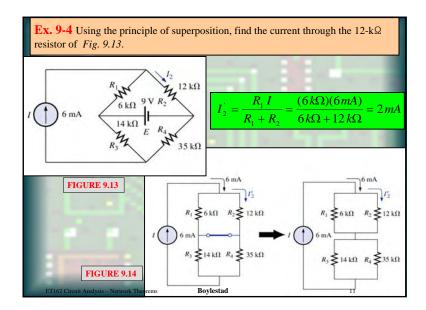


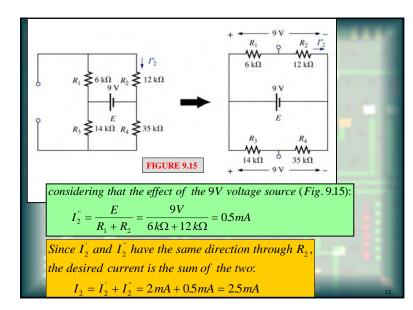


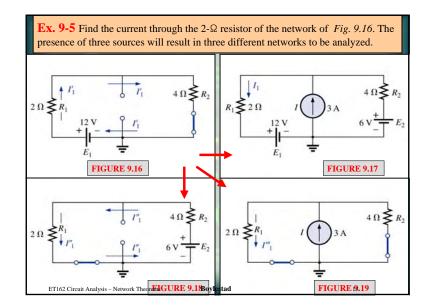


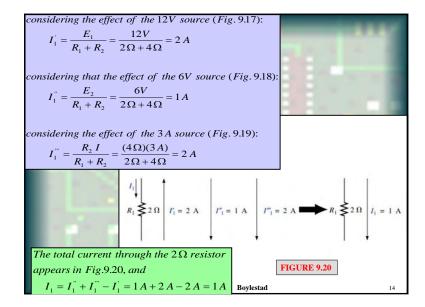


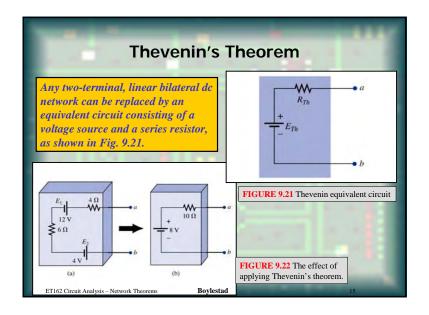


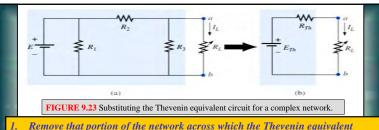




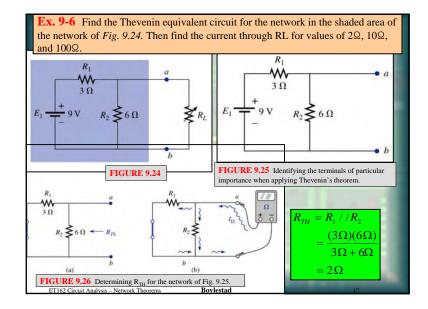


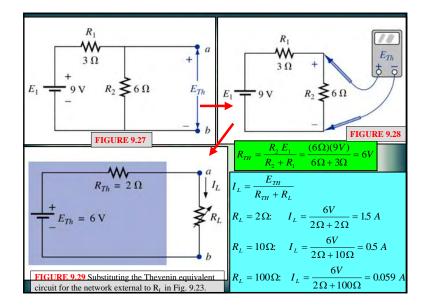


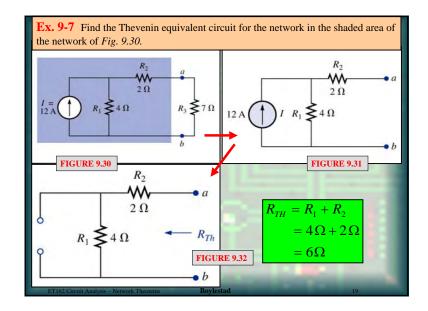


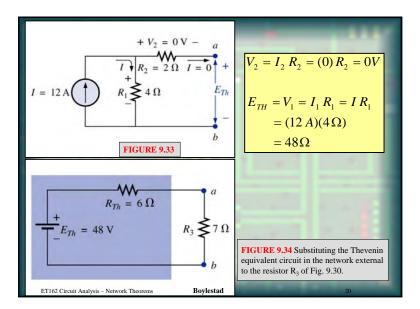


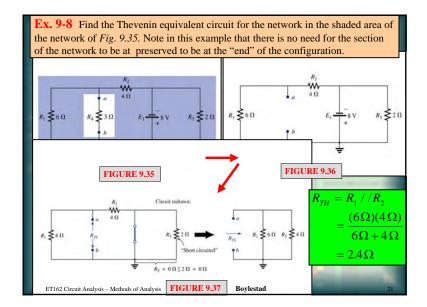
- 1. Remove that portion of the network across which the Theventh equivalent circuit is to be found. In Fig. 9.23(a), this requires that the road resistor R_L be temporary removed from the network.
- 2. Make the terminals of the remaining two-terminal network.
- Calculate R_{TH} by first setting all sources to zero (voltage sources are replaced by short circuits, and current sources by open circuit) and then finding the resultant resistance between the two marked terminals.
- 4. Calculate E_{TB} by first returning all sources to their original position and finding the open-circuit voltage between the marked terminals.
- Draw the Thevenin equivalent circuit with the portion of the circuit previously removed replaced between the terminals of the equivalent circuit.

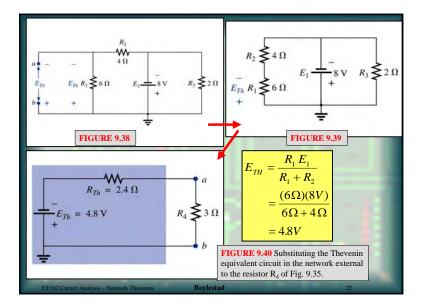


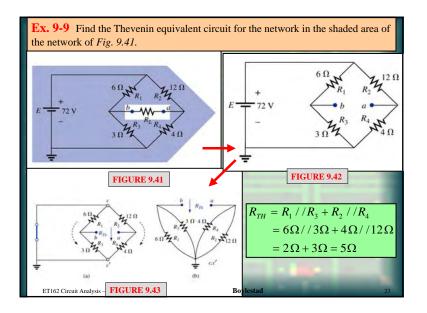


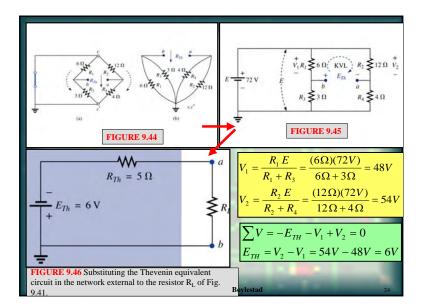


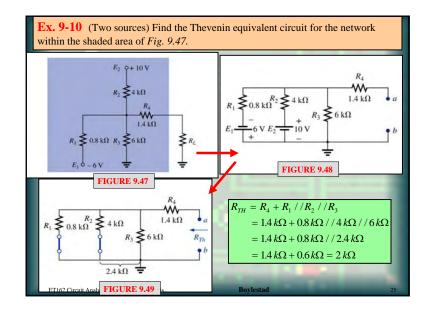


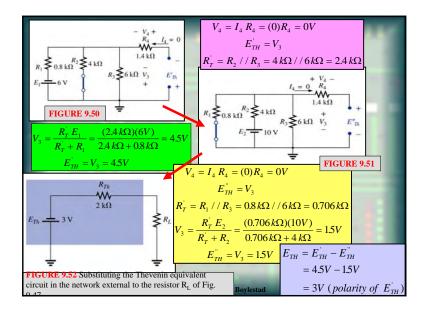


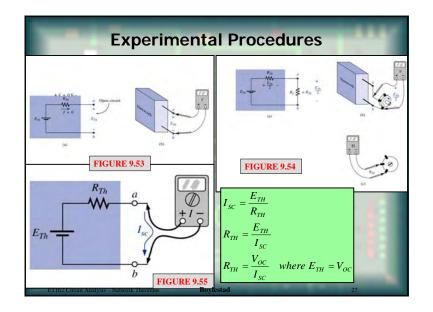


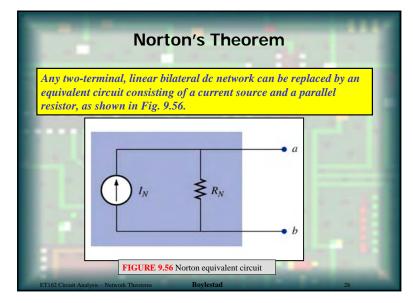




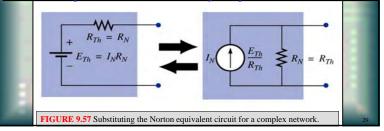


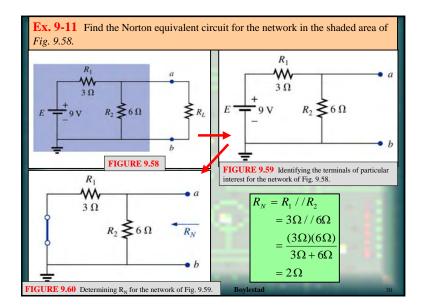


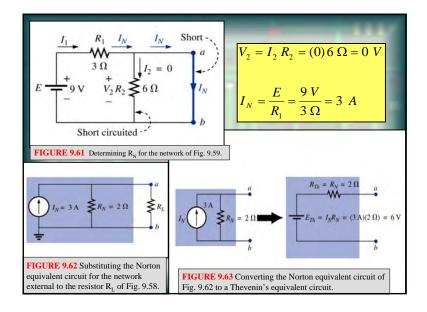


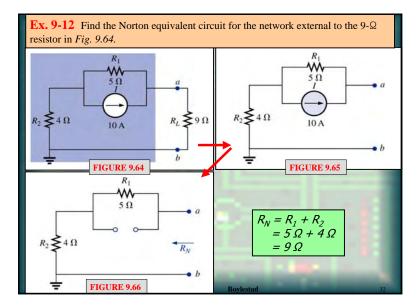


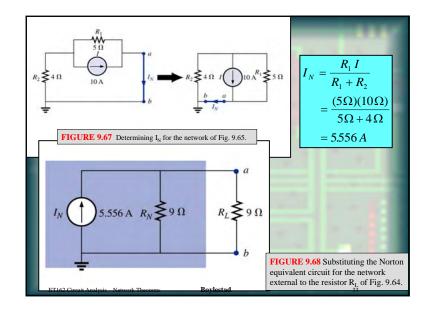
- 1. Remove that portion of the network across which the Thevenin equivalent circuit is found.
- 2. Make the terminals of the remaining two-terminal network.
- 3. Calculate R_N by first setting all sources to zero (voltage sources are replaced by short circuits, and current sources by open circuit) and then finding the resultant resistance between the two marked terminals.
- 4. Calculate I_N by first returning all sources to their original position and finding the short-circuit current between the marked terminals.
- 5. Draw the Norton equivalent circuit with the portion of the circuit previously removed replaced between the terminals of the equivalent circuit.

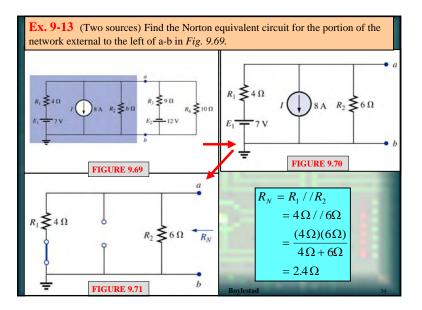


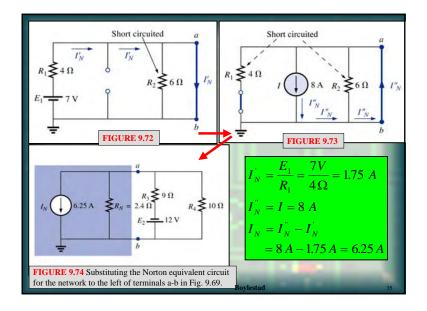


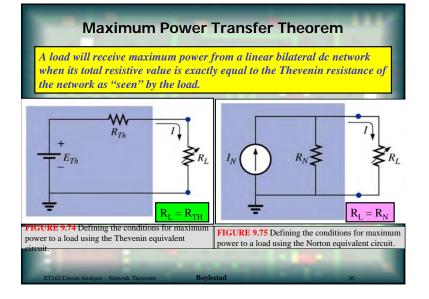


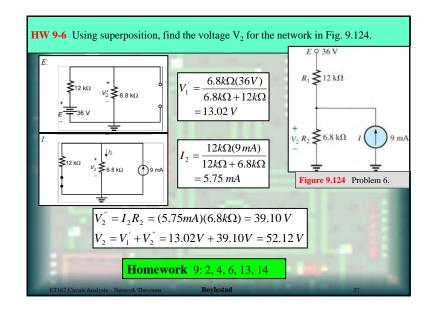




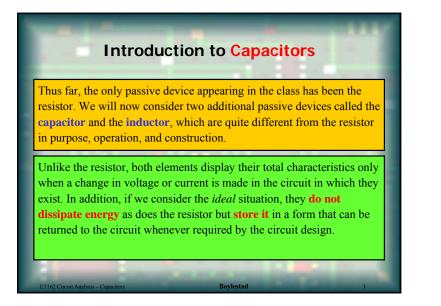


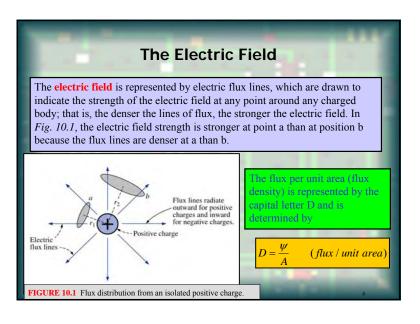


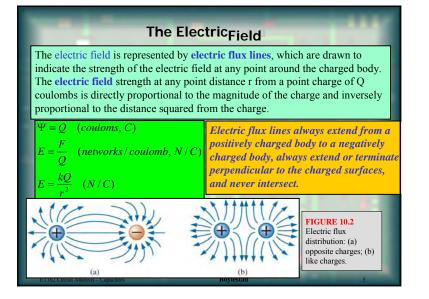


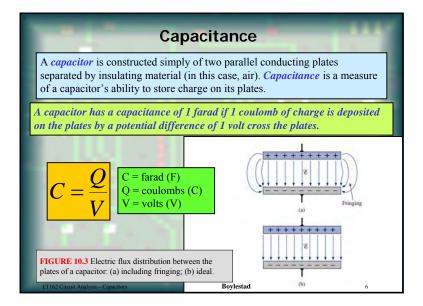


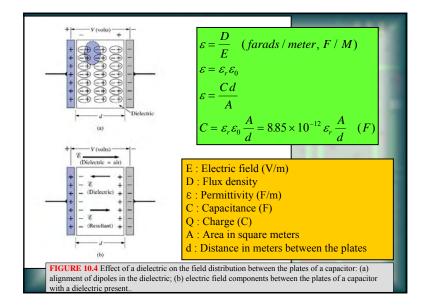


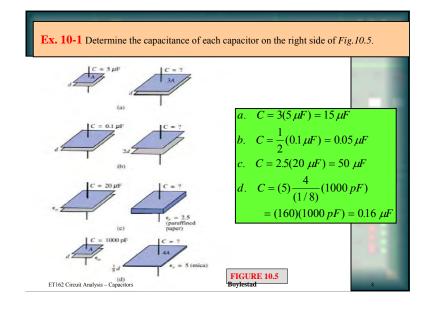


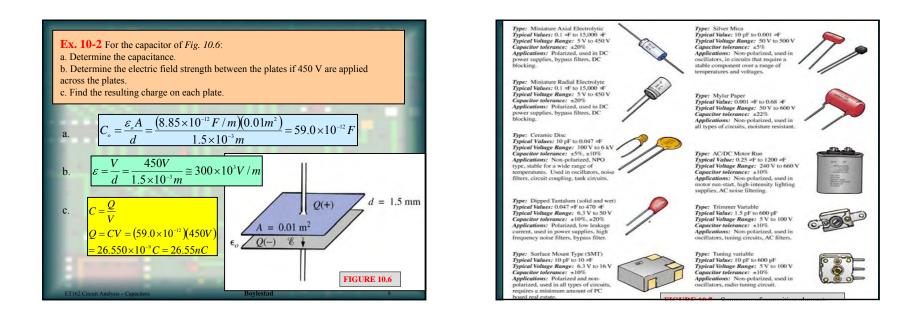


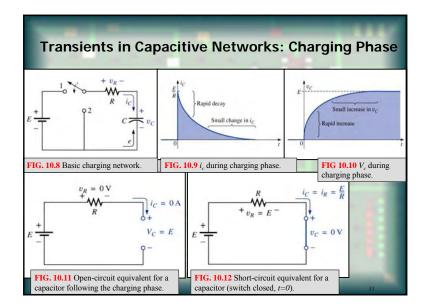


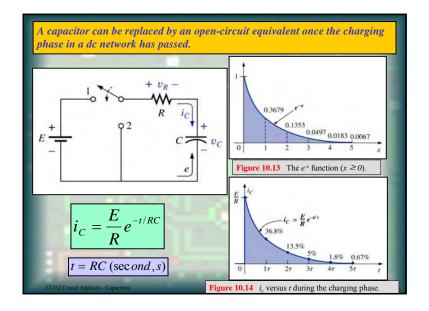


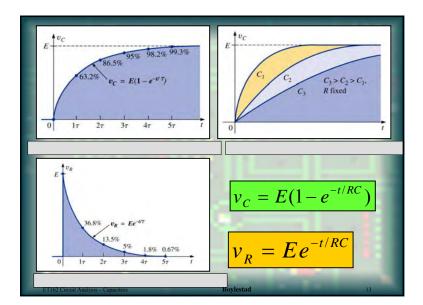


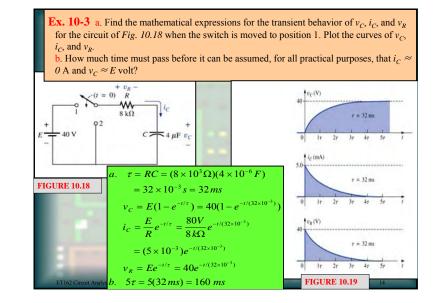


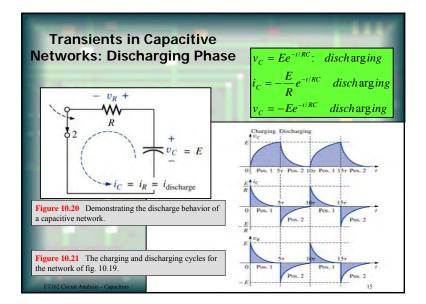


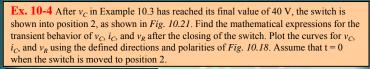


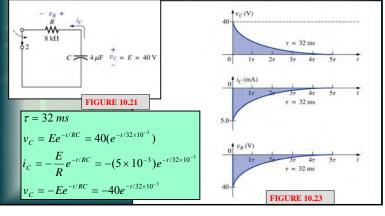


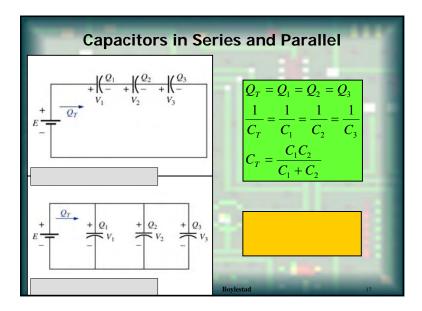


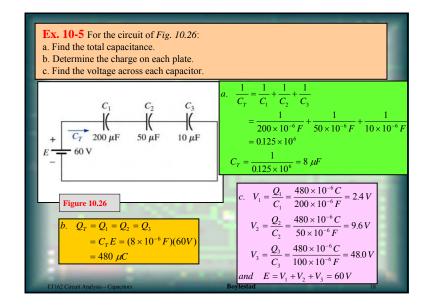


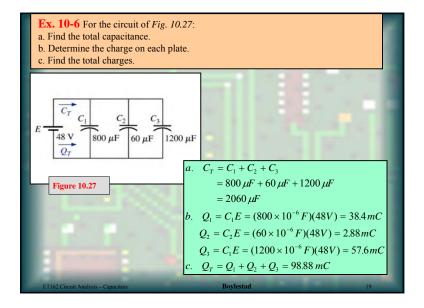


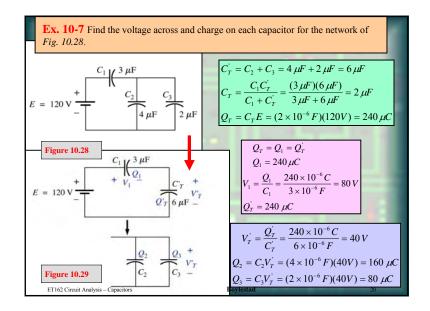


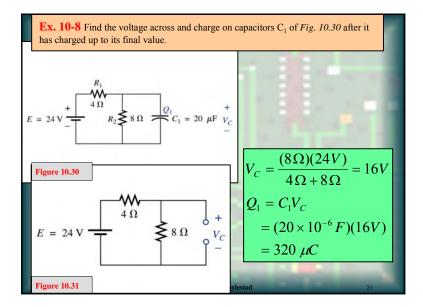


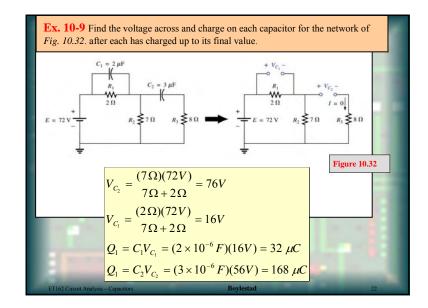


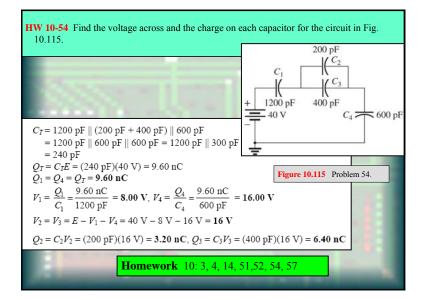


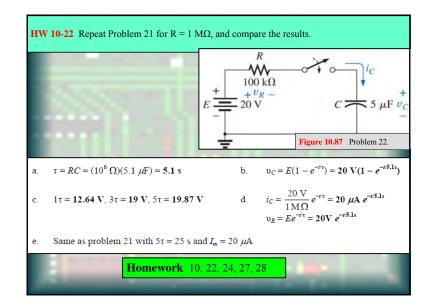


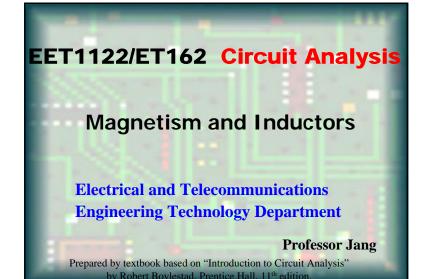




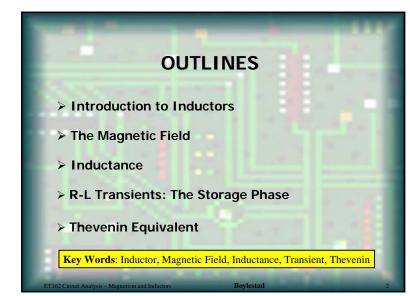








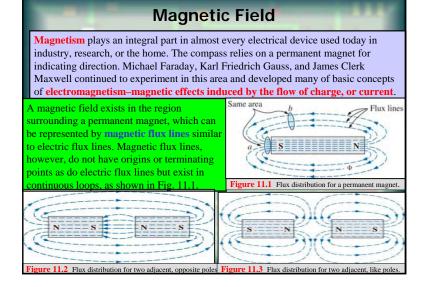
<section-header>

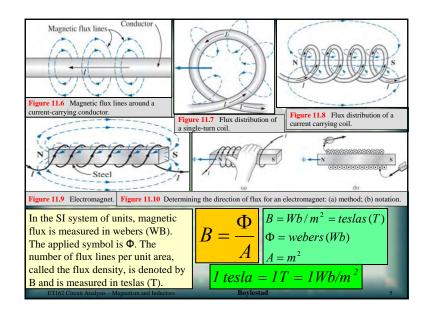


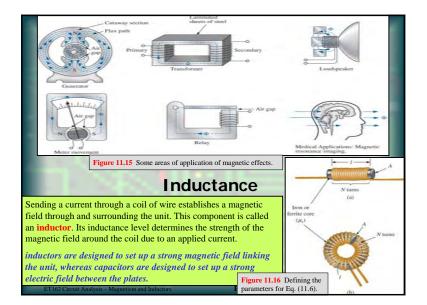
Introduction to Inductors

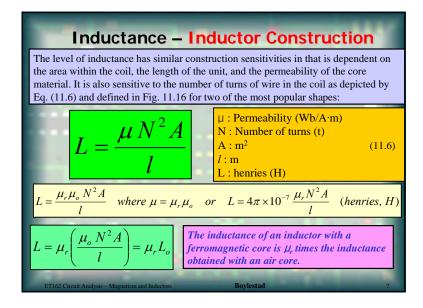
Three basic components appear in the majority of electrical/electronic systems in use today. They include the resistor and the capacitor, which have already been introduced, and the **inductor**, to be examined in detail in this module. Like the capacitor, *the inductor exhibits its true characteristics only when a charge in voltage or current is made in the network*.

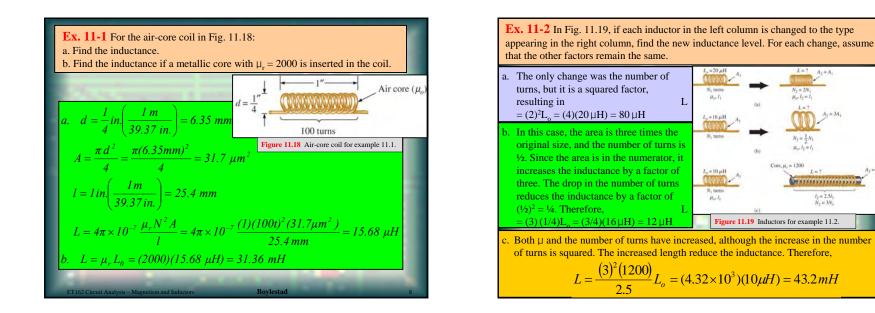
Recall from previous module that a capacitor can be replaced by an open-circuit equivalent under steady-state conditions. You will see in this module that *an inductor can be replaced by a short-circuit equivalent under steady-state conditions*. Finally, you will learn that while resistors dissipate the power delivered to them in form of heat, ideal capacitors store the energy delivered to them in the form of an electric field. Inductors are like capacitors in that they also store the energy delivered to them_but in the form of a magnetic field.

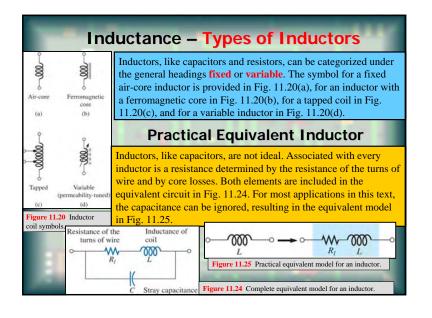


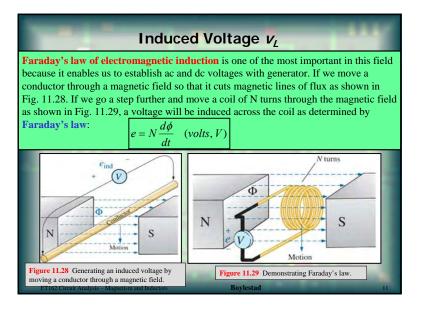




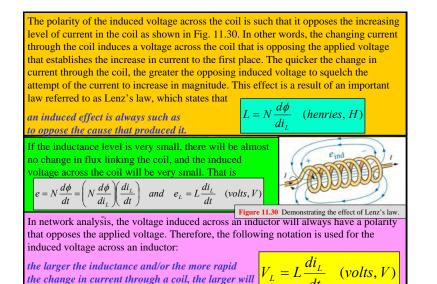


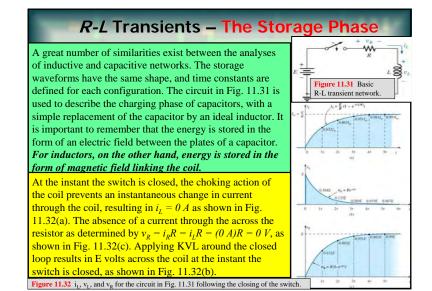


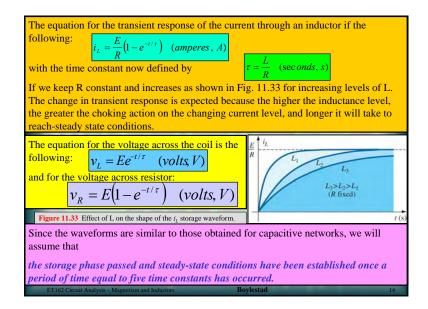


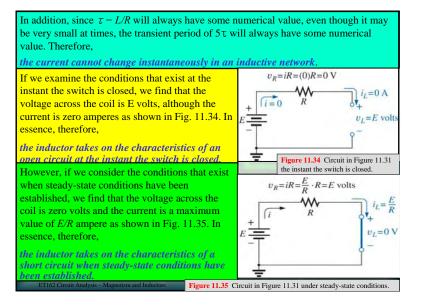


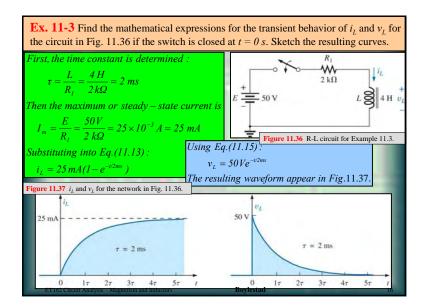
 $l_2 = 2.5 l_1$ $N_2 = 3N_1$

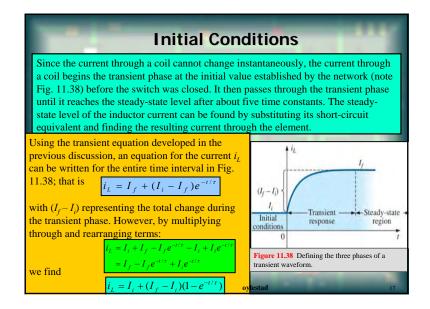








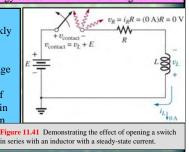


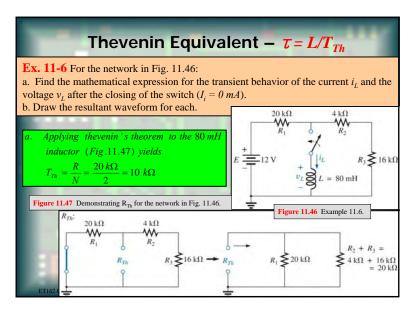


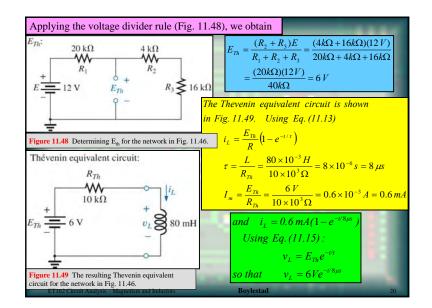
R-L Transients – The Release Phase

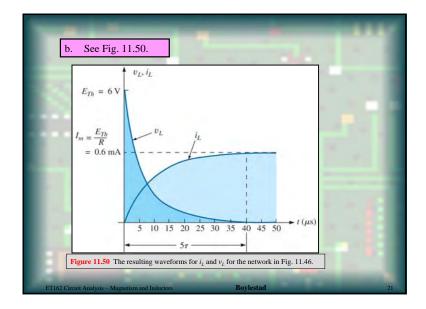
In the analysis of R-C circuits, we found that the capacitor could hold its charge and store energy in the form of an electric field for a period of time determined by the leakage factors. In R-L circuits, the energy is stored in the form of a magnetic field established by the current through the coil. Unlike the capacitor, however, an isolated inductor cannot continue to store energy, because the absence of a closed path causes the current to drop to zero, releasing the energy stored in the form of a magnetic field.

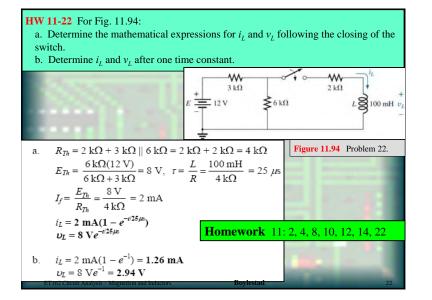
If the series R-L circuit in Fig. 11.41 reaches steady-state conditions and the switch is quickly opened, a spark will occur across the contacts due to the rapid change in current di/dt of the equation $v_L = L(di/dt)$ establishes a high voltage v_L across the coil that, in conjunction with the applied voltage E, appears across the points of the switch. This is the same mechanism used in the ignition system of a car to ignite the fuel in the cylinder.



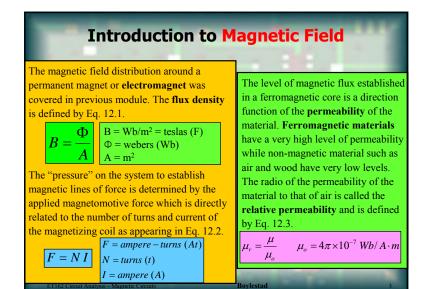


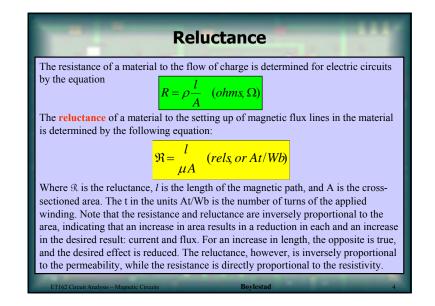


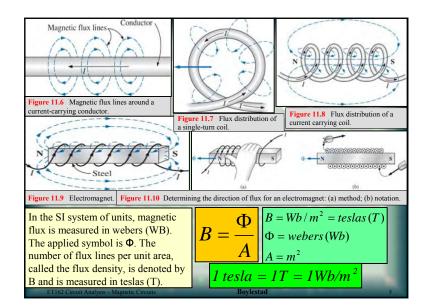


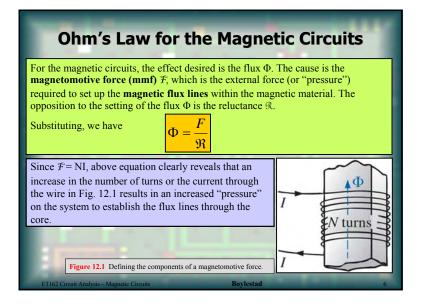


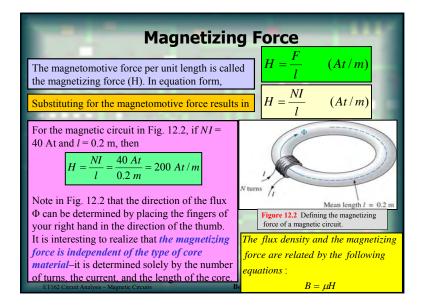


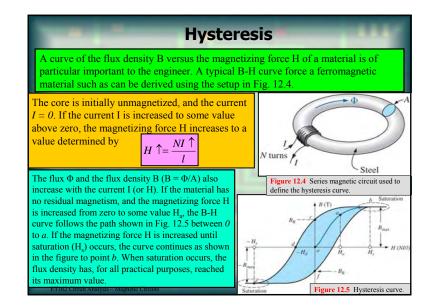








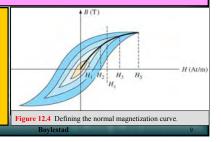




If the magnetizing force is reduced to zero by letting I decrease to zero, the curve follows the path of the curve follows the path of the curve between b and c. The flux density B_R , which remains when the magnetizing force is zero, is called the residual flux density. It is this residual flux density that makes it possible to create permanent magnets. If the current I is reserved, developing a magnetizing force, -H, the flux density when $-H_d$ is reached. The magnetizing force $-H_d$ required to "coerce" the flux density to reduce its level to zero is called the coercive force. As the force -H is increased until saturation again occurs and is then reserved and brought back to zero, the path *def* results. If the magnetizing force is increased in the positive direction (+H), the curve traces the path shown from f to b. the entire curve represented by *bcdefb* is called the hysteresis curve.

If the entire cycle is repeated, the curve obtained for the same core will be

determined by the maximum H applied. Three hysteresis loops for the same material for maximum values of H less than the saturation value are shown in Fig. 12.6. In addition, the saturation curve is repeated for comparison purposes.



Ampere's Circuital Law				
The similarity between the analyses of electric and magnetic circuits has been demonstrated to some extent for the quantities in Table 12.1.				
If we apply the "cause" analogy to KCL ($\sum_{i} V$ = 0), we obtain the following: $\sum_{i} f = 0$ (for magnetic Circuits)			Table 12.1	
			Electric Circuit	Magnetic Circuits
		Cause	Е	F
which, in words, states that the algebraic sum of the rises and drops of the mmf around a closed		Effect	Ι	Φ
loop of a magnetic circuits is equal to zero.		Opposition	R	R
Above equation is referred as Ampere's circuital law. When it is applied to magnetic circuits, sources of mmf are expressed by the equation $\mathcal{F} = NI$ (At) The equation for the mmf drop across a portion of a magnetic circuit can be found by applying the relationships listed in Table 12.1; that is, for electric circuits, V = IR	Resulting in the following for magnetic circuits $\mathcal{F} = \Phi \Re$ (At) Where Φ is the flux passing through a section of the magnetic circuit and R is the reluctance of that section. A more practical equation for the mmf drop is, $\mathcal{F} = H l$ (At) where H is the magnetizing force on a section of a magnetic circuit and l is the length of the section.			

